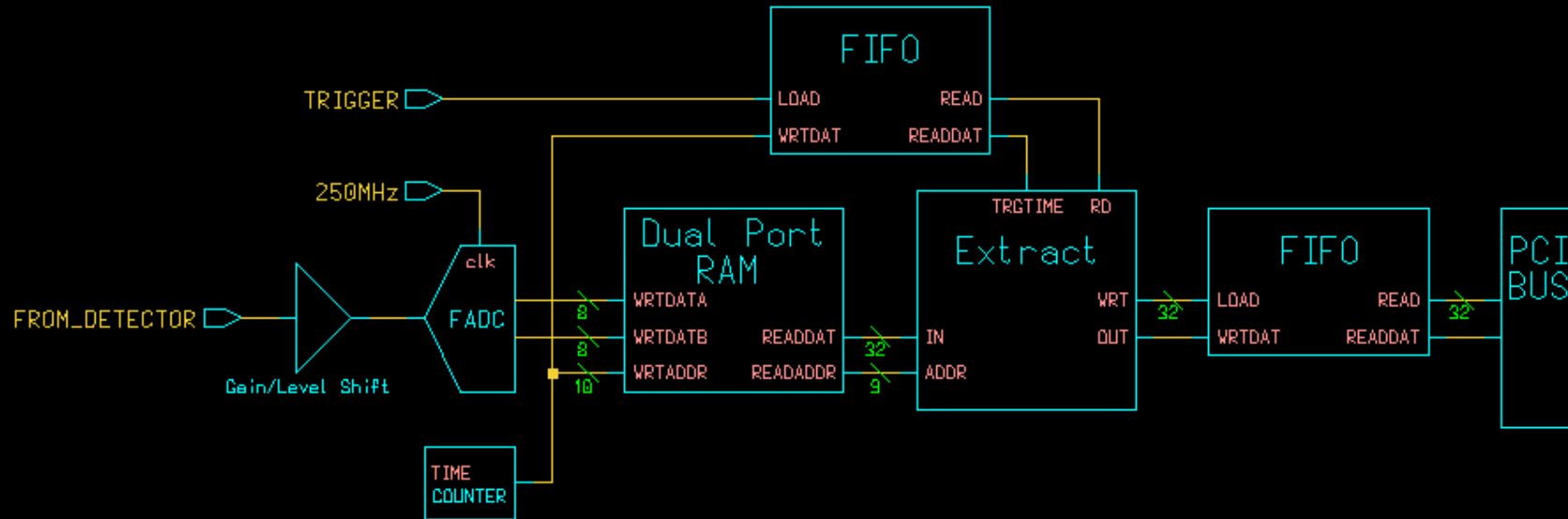


Single channel FADC prototype



Test bed for:

- SPT converter chip
- Xilinx chip and software
- Mentor PCB & FPGA software
- Intellectual Property (PCI core)
- Robotic assembly

