

Subject: Re: [EXTERNAL] Status of the TAC in Hall D

From: miskimen@physics.umass.edu

Date: 3/9/22, 9:07 AM

To: Alexander Somov <somov@jlab.org>

CC: Elton Smith <elton@jlab.org>, Mark Ito <marki@jlab.org>, beni zihlmann <benizihlmann@gmail.com>, David Lawrence <davidl@jlab.org>

Thanks for the speedy reply.

Unfortunately, because of the installation of the iron absorbers and MWPC's downstream of FCAL, I don't think we can install the CCAL.

I'll pass this information about the TAC on to the CPP group, and we'll discuss.

Cheers

Rory

Sent from my iPad

On Mar 8, 2022, at 5:35 PM, Alexander Somov <somov@jlab.org> wrote:

Hi Rory,

The last time I used TAC was about two years ago. We used CCAL as TAC in PrimEx and other recent runs; it worked very well.

This is what I did with the TAC (about 2 years ago):

I annealed it to reduce radiation damage (visually it was well recovered) and replaced the PMT (signal pulse shapes looked bad). *The performance of the TAC significantly degraded, that's why we had to do this.*

We tested it after that on the bench test stand, and got installed in the hall, but never tested it with beam. In general, I think that it should be operational.

The best would be to install the CCAL during the CPP run and use it as TAC (I will talk to Tim about this, he had some concerns regarding entrance to the platform)

Cheers,

Sasha

From: miskimen@physics.umass.edu <miskimen@physics.umass.edu>

Sent: Tuesday, March 8, 2022 10:05 AM
To: Alexander Somov <somov@jlab.org>
Subject: [EXTERNAL] Status of the TAC in Hall D

Hi Sasha,

I'm writing to ask about the status of the TAC. Have you used it at all during PrimEx running? Hovanes thought you were most likely to know what it's condition is.

Thanks
Rory

Sent from my iPad

On Jan 20, 2022, at 10:31 AM, Alexander Somov <somov@jlab.org> wrote:

Hi Iliya,

I've already started working on this. I am in the middle of updating the firmware.

Cheers,
Sasha

From: Ilya Larin <ilarin@jlab.org>
Sent: Thursday, January 20, 2022 9:54 AM
To: Alexander Somov <somov@jlab.org>; Sergey Furletov <furletov@jlab.org>
Cc: halld-cpp@jlab.org <halld-cpp@jlab.org>; Eugene Chudakov <gen@jlab.org>
Subject: CPP firmware testing

Hello Sasha and Sergey.

Time is running quickly and it is already a month past since you collected the data for the CPP trigger firmware testing. We have four months left before the experiment is going to run and it may take a couple of months to fix any potential problem you may find. That's coming to the high-priority task that still needs to be done. That's why CPP people are more and more concerned about not hearing anything from your side and ask me to inquire about the progress and any certain plans you may have in this direction. Please keep us in touch about this matter.

Thank you.
Ilya

Halld-cpp mailing list
Halld-cpp@jlab.org
<https://mailman.jlab.org/mailman/listinfo/halld-cpp>