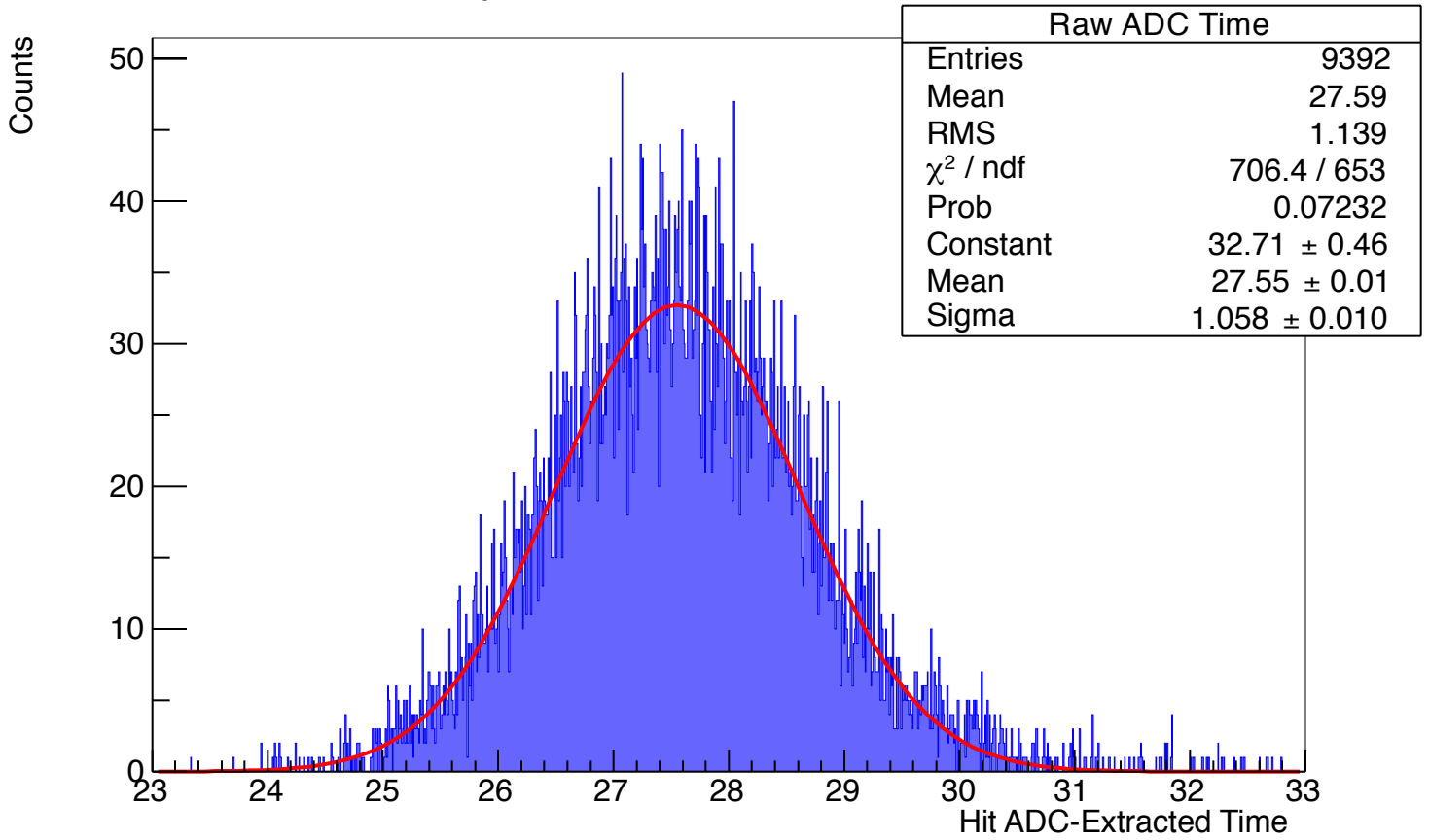
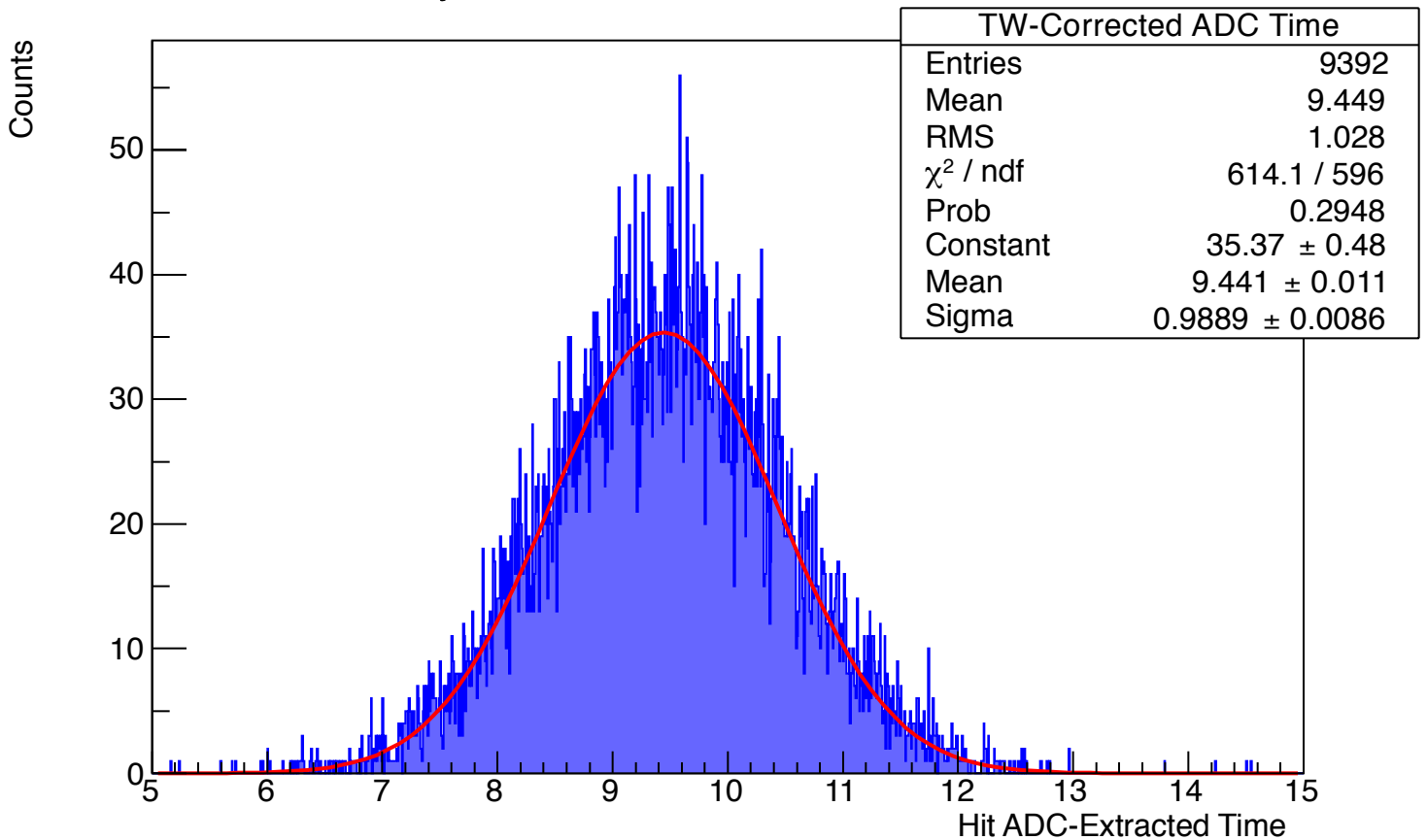


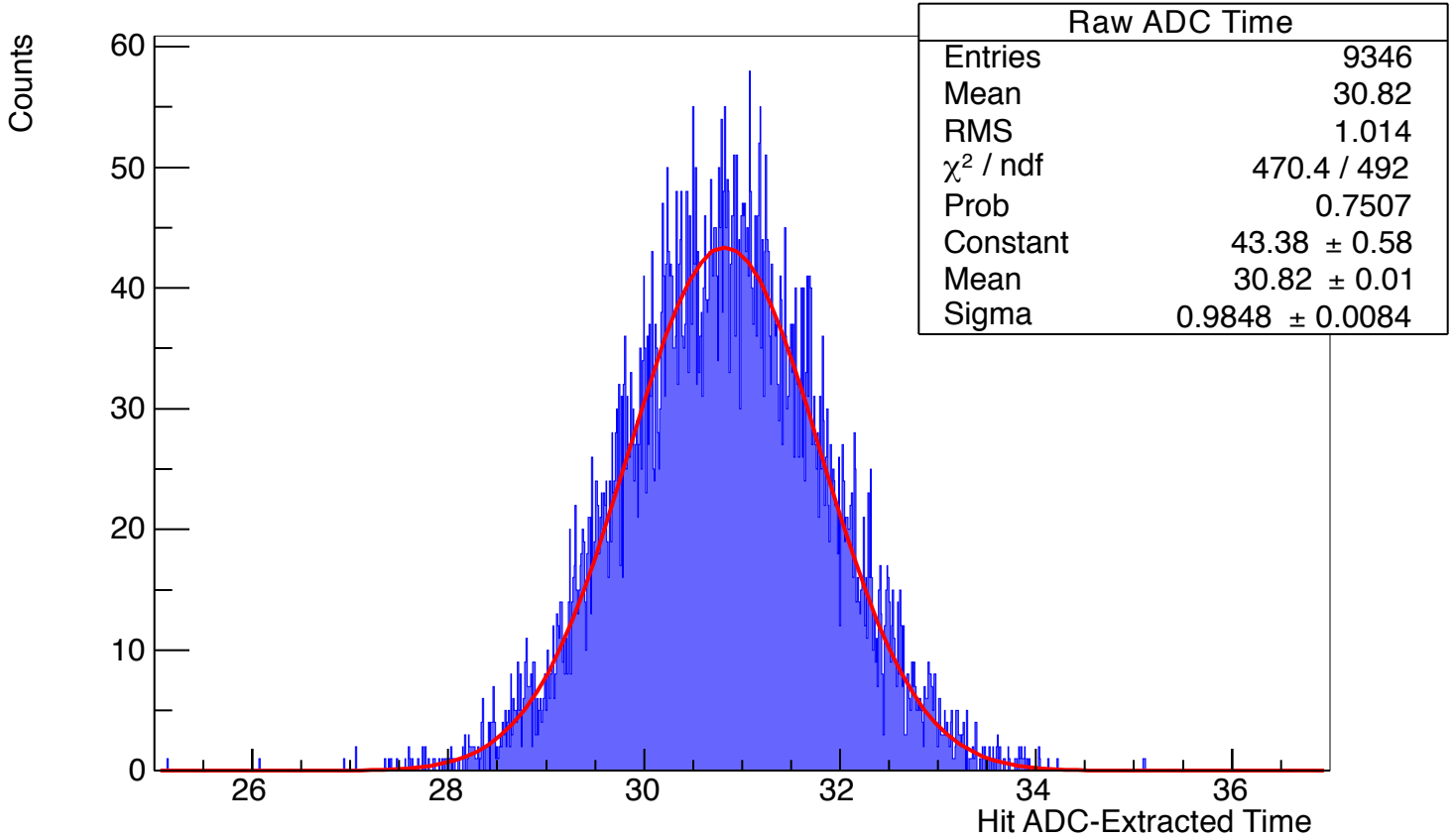
Module 1, Layer 3, Sector 3, End 0 - Raw ADC Time



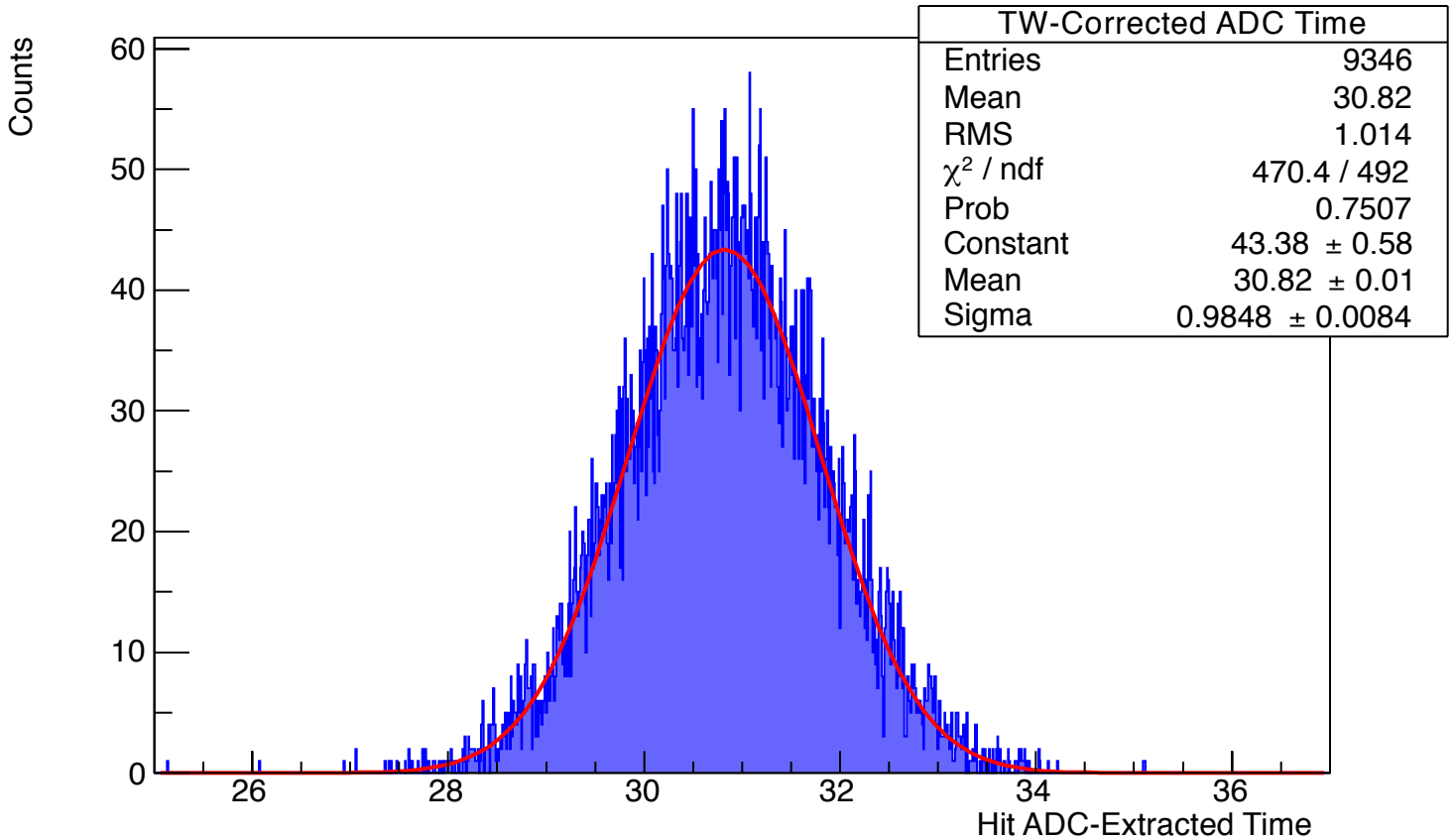
Module 1, Layer 3, Sector 3, End 0 - TW-Corrected ADC Time



Module 1, Layer 3, Sector 3, End 0 - Raw ADC Time



Module 1, Layer 3, Sector 3, End 0 - TW-Corrected ADC Time



Module 1, Layer 3, Sector 3, End 0 - Time-Walk Fit: $C_0 + C_1/(E-C_3)^{C_2}$

