12GeV Trigger Workshop
Christopher Newport University, Newport News, VA

Thursday 8 July, 2010

Registration Link:
http://www.jlab.org/Hall-D/meetings/online_workshop2010/Registration/Register.php

For conference call participation:
1) Dial toll free 1-800-377-8846 (From Canada dial 1-888-276-7715)
2) Enter the participant pass code, 29917863 followed by the # sign

Session I: 9:30 --> 10:45
Welcome and Overview   -Chris Cuevas

-- Goals for the workshop
1. Present recent development and status of trigger hardware FY10
2. Review requirements for trigger system modules that have not been completed and verify that the new requirements established from last year's workshop have not changed.
3. Open discussion about system level commissioning and other 'Tools' required for testing and verification of the overall trigger system for each Hall.

- Topics:
  Hardware Status:
    Flash ADC Revision 1 --
    Crate Trigger Processor
    Signal Distribution (Clock, Trigger, Sync)
    Trigger Interface and Distribution
    Sub-System Processor
    Global Trigger Processor

Coffee Break: 10:45 --> 11:00

Session II: 11:00 --> 12:45
DAQ System Update - David Abbott

Trigger Interface and Distribution -- William Gu

Lunch 12:45 --> 14:00: [Plenty of locations near the campus]

Session III: 14:00 --> 15:45
Global Trigger and Commissioning Alex Somov and Hai Dong

Tools for high level trigger algorithms in Sub-System Processor (SSP) and/or Global Trigger Processor (GTP): Ben Raydo

Coffee break: 15:45 --> 16:00

Session IV:-16:00 --> 17:30
Recent experience with Flash ADC-250 trigger features on PREx: Brad Swatzky

Trigger system applications for CLAS12: Sergey Boyarinov

-- Adjourn --

Dinner: [6:30pm Sharp!]

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