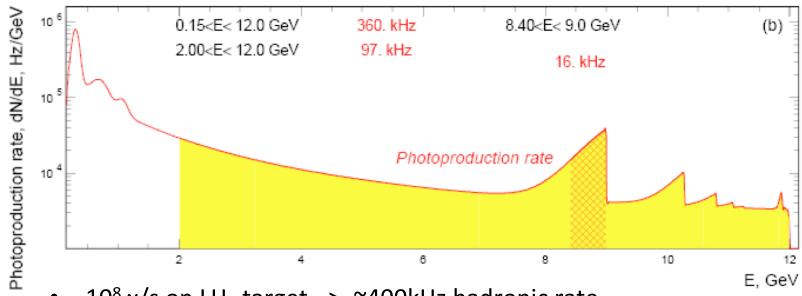
Hall-D L3 trigger status



David Lawrence JLab July 22, 2016

From PR12-13-003 (GlueX strangeness proposal)

... we propose a gradual increase in the photon flux towards the GlueX design of 10^8 y/s in the peak of the coherent bremsstrahlung spectrum (8.4 GeV < Ey < 9.0 GeV). Yield estimates, assuming an average flux of 5×10^7 y/s, are presented.



- 10^8 y/s on LH₂ target -> ~400kHz hadronic rate
- L1 trigger goal is to cut away ~50% leaving 200kHz
- L3 trigger goal is to reduce by ~90% leaving 20kHz actual: ~75%
- Early simulation suggested ~15kB/event actual: 14kB + 0.05kB/nA*
 - 15kB/event @ 200 kHz = 3000 MB/s (front end)
 - L3 reduction by factor of 10 = 300MB/s to RAID disk

L1 trigger rates

Fall 2014: 2kHz

Spring 2015: 3.5kHz

Spring 2016: 30kHz

Data Rates

"Achieved" means with actual data while it was being acquired. In some cases, offline testing has achieved significantly higher rates.

> 554TB written to tape in Spring 2016 commissioning run

ROC Spec: 100MByte/sec Achieved: 30-80MByte/sec ROC **ROC** Event Builder (2 stages) **ROC** Spec: 300MByte/sec ROC Achieved: 600MByte/sec 800MByte/sec **ROC** Event 72TB x2 Recorder ROC RAID disk AOGbit 18 ROC Spec: 300MByte/sec (~50 ROCs) Achieved: 600MByte/sec 800MByte/sec

Tape Library 2×10Gbit ethernet

Spec: 300MByte/sec Achieved: 450MByte/sec

2000MByte/sec

L1 trigger rates

Fall 2014: 2kHz

Spring 2015: 3.5kHz

Spring 2016: 30kHz

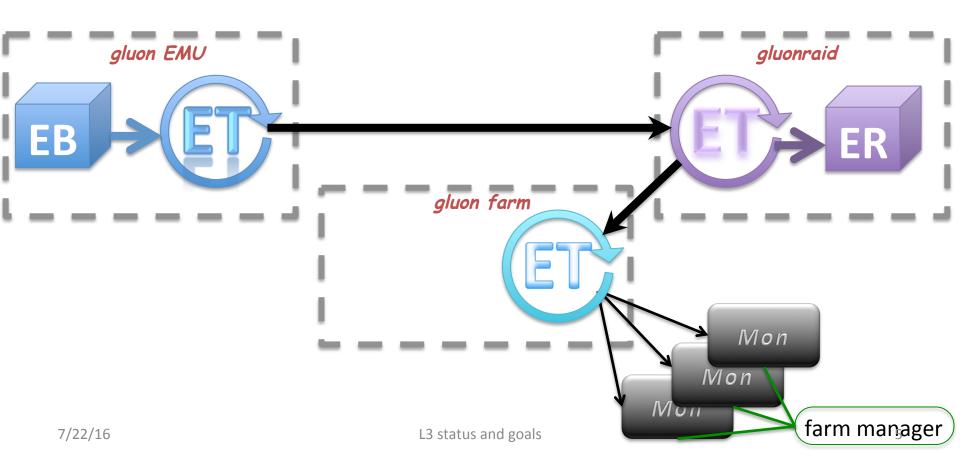
Data Rates

"Achieved" means with actual data while it was being acquired. In some ROC Spec: 100MByte/sec cases, offline testing has achieved Achieved: 30-80MByte/sec significantly higher rates. ROC 554TB written to tape in Spring **ROC** 2016 commissioning run 3000_{MByte/sec} Event Builder (2 stages) **ROC** ROC L3 farm 300MByte/sec Tape Library 40Gbit IB **ROC** Event 72TB x2 Recorder ROC 2×10Gbit ethernet RAID disk AOGbit 18 Spec: 300MByte/sec ROC Achieved: 450MByte/sec 2000MByte/sec Spec: 300MByte/sec (~50 ROCs) Achieved: 600MByte/sec 800MByte/sec

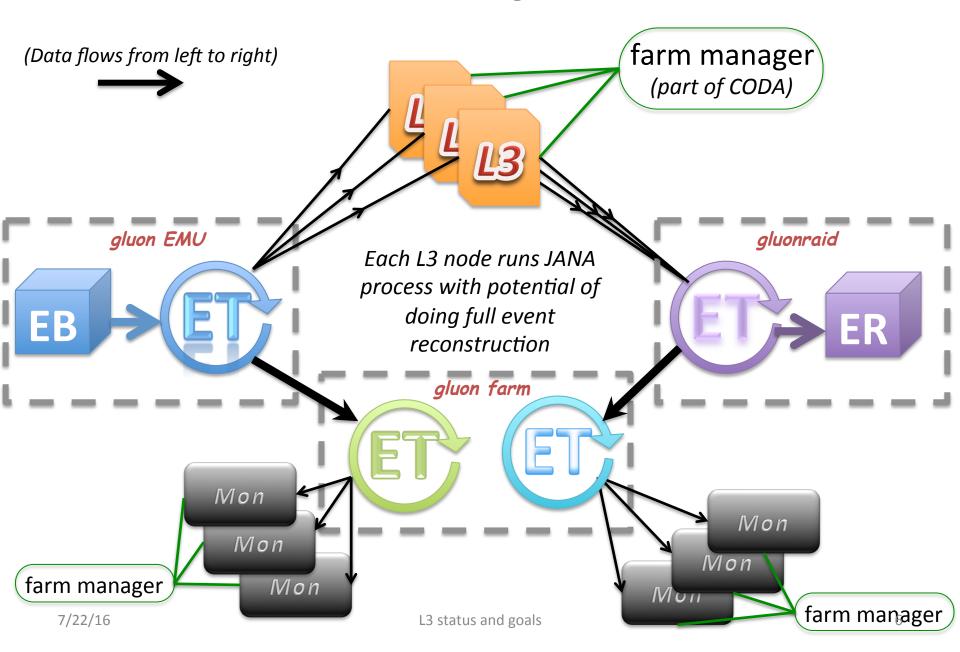
L3 and monitoring architecture

(Data flows from left to right)

Spring 2016 Configuration

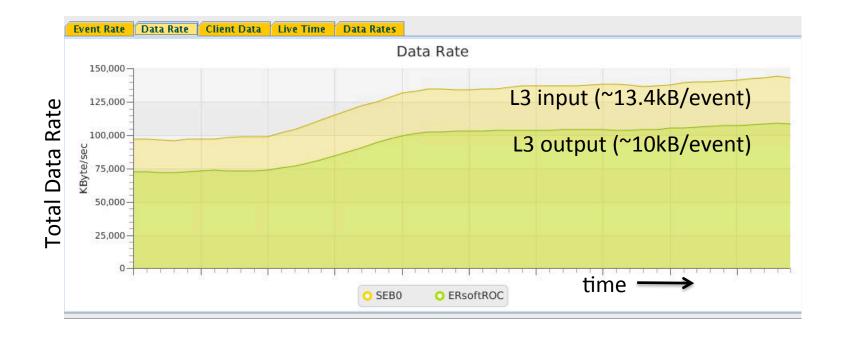


L3 and monitoring architecture

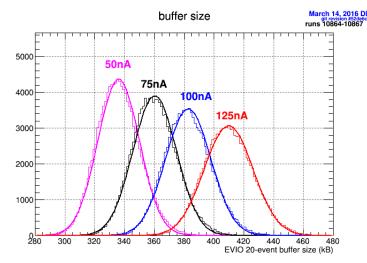


L3 running in pass-through with beam

- Events are read in blocks of 20
- Events must be disentangled and reconstituted as single events before writing to disk
- Redundant headers may be dropped to reduce event size

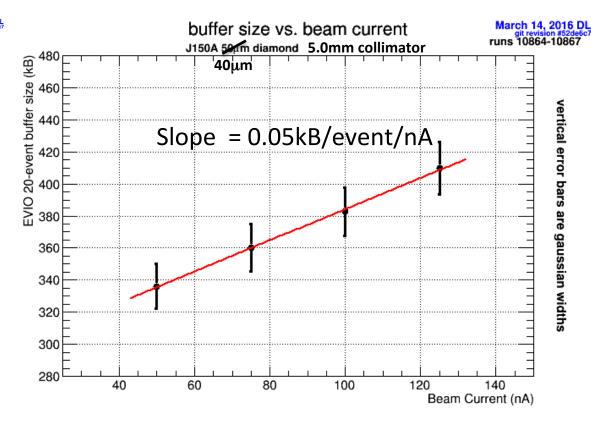


Event Size vs. Beam Current



Event Size: ~19kB

 $40\mu m$ diamond 5mm collimator $120nA = 0.8x10^7 \gamma/s$ $750nA = 5x10^7 \gamma/s$



extrapolate to $I_{beam} = 0$ \rightarrow 14.25kB/event (size of clean event with no accidentals)

Accidental data fraction (by volume):

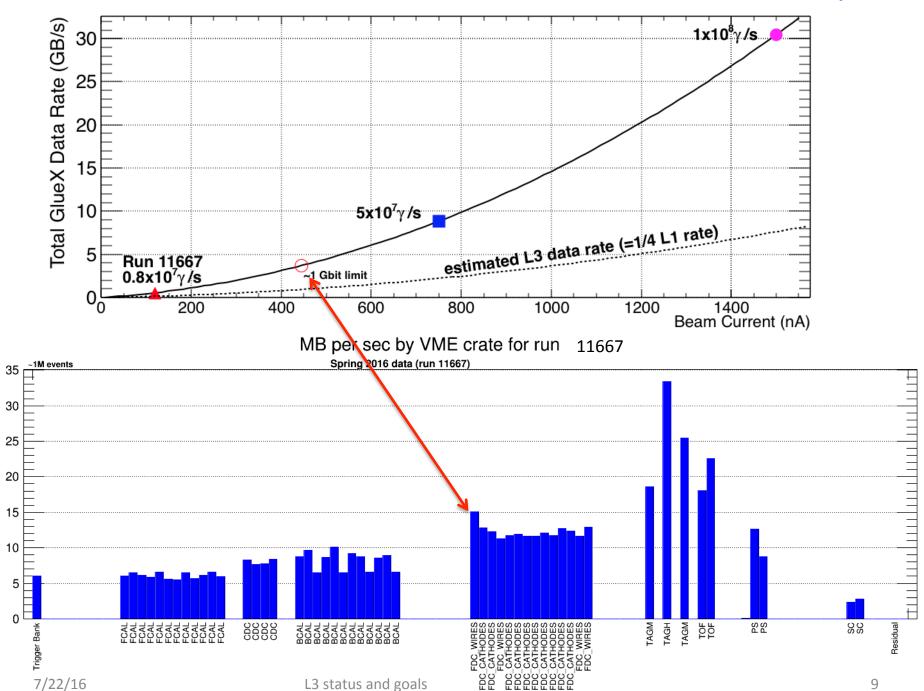
 $(0.05kB/nA)(I_{beam} nA)$

 $(0.05kB/nA)(I_{beam} nA) + (14.25kB)$

100nA: 26% of data is due to accidentals

200nA: 41% of data is due to accidentals

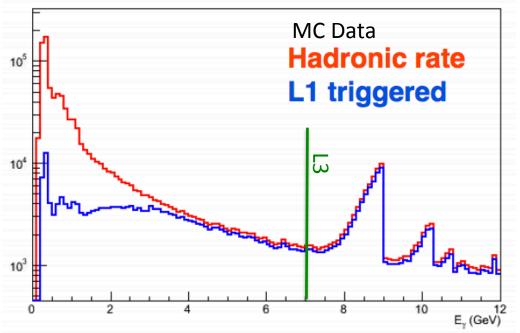
750nA: 72% of data is due to accidentals



MB/s

L3 Algorithm Strategy

- Use Multivariate Analysis such as BDT or ANN to classify events (LHCb)
- Multiple levels may be used with each level requiring more expensive input variables



- Use Multivariate Analysis such as BDT or ANN to classify events
- Multiple levels may be used with each level requiring more expensive input variables
- Use fully reconstructed, real data to provide training samples (signal and background)
- Simultaneously pursue with simulated data

Reconstruction times survey

Time is divided by #calls and #threads

J1A50 50 um radiator, PERP, 7 mode, 105 nA beam cur rent, 27 kHz event rate, live time ~70%, LH2 fill, 5 mm collimator, 83 M total events

hd_rawdata_010913_060.

input file:

phys_skim.evio

| | events | ALGORITHM | INPUT OBJECT RECO TIME [s] | | RECO TIME/event [ms] |
|---|--------|-----------------|----------------------------|-----------|----------------------|
| I | 10000 | nominal reco | DNeutralShower | 4.410 | 0.028 |
| | DATA | approx reco | DBCALShower | 2.750 | 0.017 |
| | | approx reco | DFCALShower | 8.010 | 0.050 |
| | | full tracking | DTrackTimeBased | 18669.810 | 116.69 |
| | | approx tracking | DTrackWireBased | 7397.300 | 46.23 |

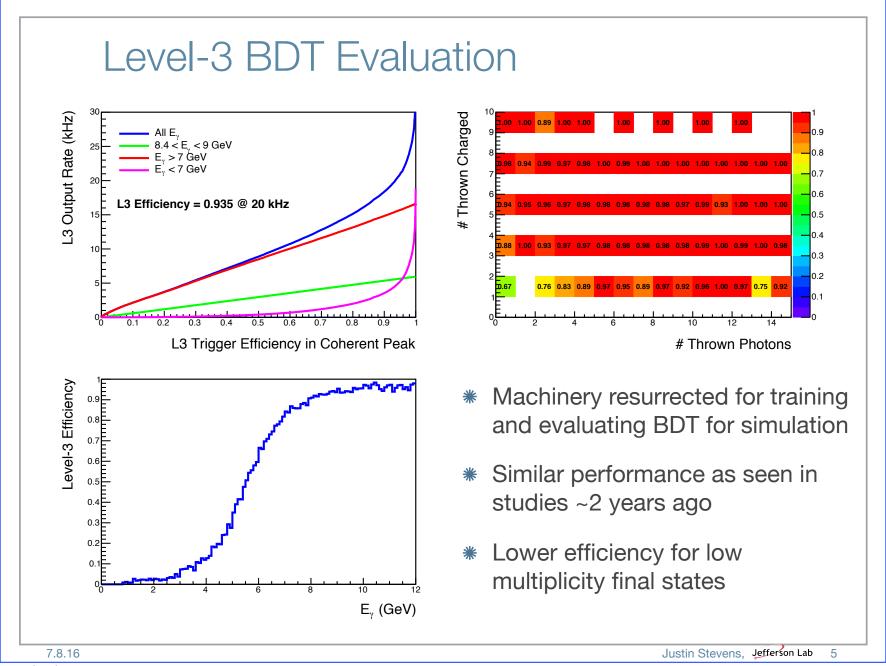
hdgeant_smeared_14980

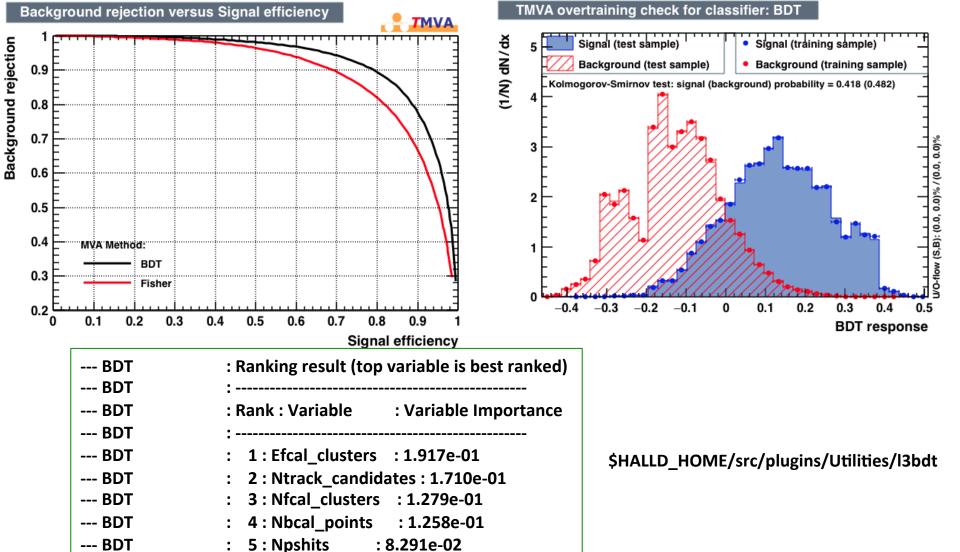
| events | ALGORITHM | INPUT OBJECT | RECO TIME [s] | RECO TIME/event [ms] |
|--------|-----------------|-----------------|---------------|----------------------|
| 10000 | nominal reco | DNeutralShower | 5.730 | 0.036 |
| MC | approx reco | DBCALShower | 3.430 | 0.021 |
| IVIC | approx reco | DFCALShower | 7.010 | 0.044 |
| | full tracking | DTrackTimeBased | 23878.840 | 149.243 |
| | approx tracking | DTrackWireBased | 12778.340 | 79.865 |

n.b. parsing of evio data takes 0.080 – 0.400 ms/event

6

11





--- BDT

--- BDT

--- BDT

--- BDT

7/22/16

--- BDT : 10 : Nbcar_clusters : 4.875e-02

12: Npschits

8: Ntof

6 : Ebcal_points

7: Ebcal clusters : 6.445e-02

: 0.000e+00

: 6.424e-02

: 7.186e-02

Estimated number of L3 nodes

- From Spring 2016 running: \sim 30kHz/0.8x10⁷ γ /s
- For $5x10^7 \text{ y/s}$: ~190kHz
- 2013 Ivy Bridge nodes
 - 2.5-13kHz parsing only
 - 4kHz parsing+neutrals recon
 - 2kHz final algorithm (rough estimate)
- Newer nodes assume x2 faster
 - 4kHz/node
- Total number of nodes required:
 - -190kHz/4kHz = 48



Schedule



- Continue L3 development, testing infrastructure and MVA efficiency through Fall
- Integrate testing schedule into Fall 2016 run
- If confident, take portion of Spring 2017 data in pass-through mode
 - Events on tape will be reconstituted single events
 - Event tagging may be used if fast algorithm available
- Full L3 deployment Fall 2018

Potential Issues

- fADC125 high rate performance
 - Busy signals (currently being tested in Hall-D)
- Individual VME crate saturates 1Gbit link
 - 10Gbit ethernet cards in ROCS
 - Split crates (either backplanes or add additional crates)
 - Parallel module readout (VXS, or new fADC125 daughters)
- 50 ROCs pushing 9GB/s through full CODA system
 - Minimize single crate data size to allow high luminosity testing
 - May require tuning IB network or splitting streams

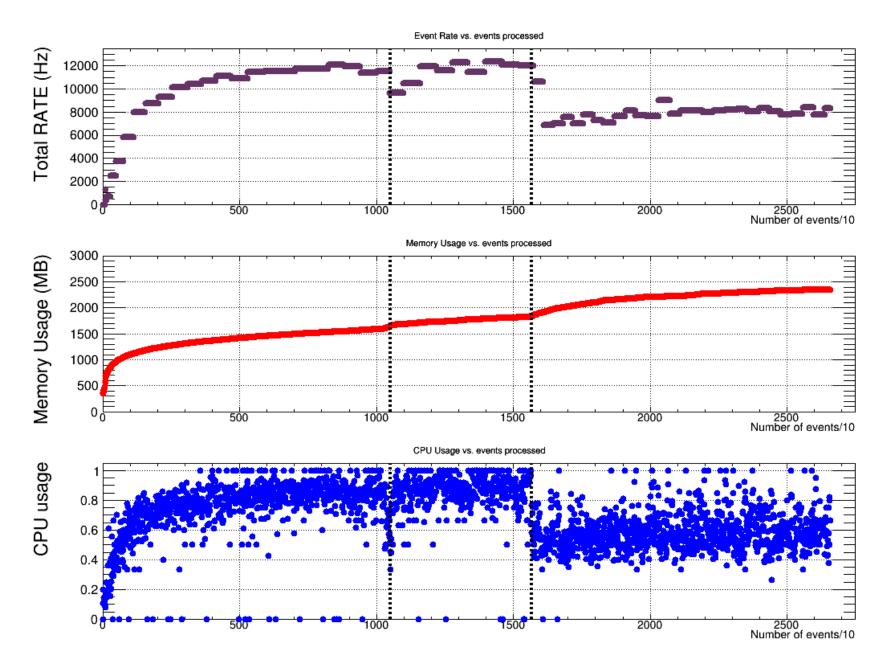
Backup Slides



Detectors At High Rate

- FDC aging effects for wires closest to beamline
- FCAL innermost blocks radiation damage
 - Curing
 - Replace with rad-hard insert

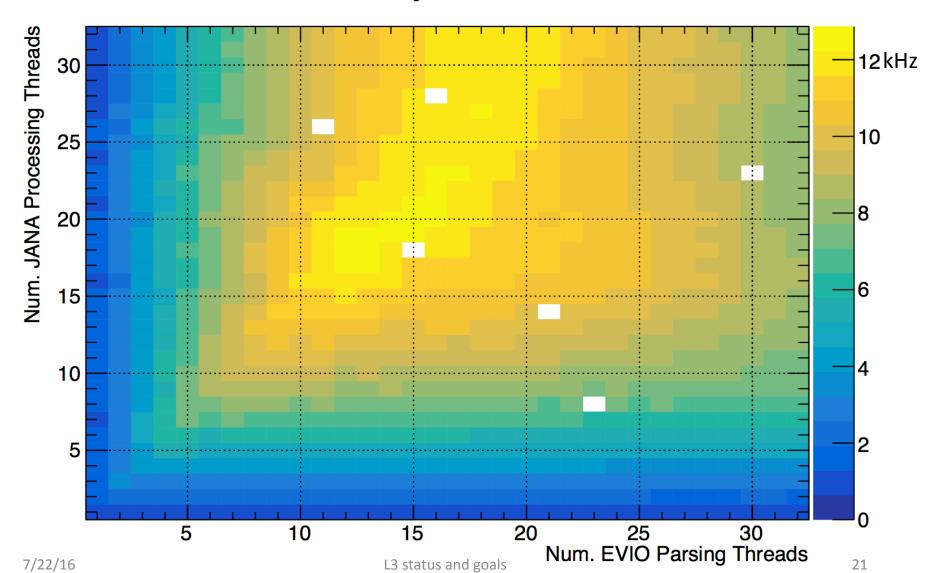
- Potential concern for the CDC and FDC is the aging of the wires: high currents in the chambers which, especially in presence of organic contamination, can result in formation of deposits on the wire surface (like polymers) and manifest as a decrease of the gas gain.
- The relevant quantity is the charge per unit wire length. For the nominal low intensity during the commissioning/ engineering running we had in the inner FDC HV sector (20 wires) about 5 uA currents, which however is concentrated to the area closest to the beam line, where we expect to have up to 1 uA/cm. In the CDC the currents on 24 inner straws was about 10 uA, but distributed evenly along each wire and between the 24 wires, corresponding to current densities of about 5 nA/cm.
- No wire aging effects at all have been observed in Ar/CO2 gas mixtures in studies with up to 1C/cm, which however corresponds to only ~12 beam-on-target days at low intensity. Up to now (??total charge/cm??) we have not seen any wire aging. Even wire aging is possible at higher intensities, it will be concentrated in a small area around the beam line.
- Conclusions: don't expect problems with the CDC; in FDC aging is possible but only in a small area around the beam.
- In order to mitigate potential aging effects due to radiation we do add alcohol to the gas. This is proven to stop the progress of aging if already present and inhibit aging from happening in the first place. As an example HallC operated their wire chambers for more than a decade now using alcohol in the gas mix and to my knowledge no aging has been observed. (This statement should be confirmed by Howard Fenker)
- One potential issue could be radiation damage to the FCAL lead glasses over the prolonged period of highluminosity running.



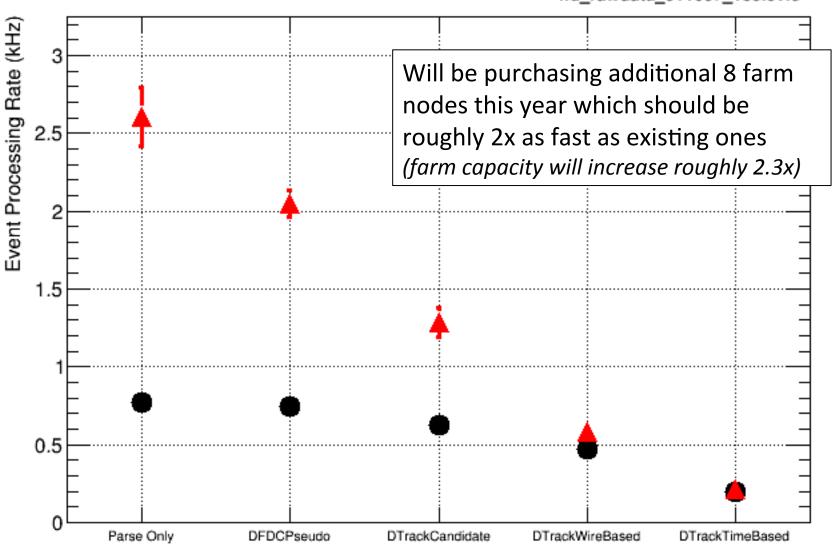
Parsing only

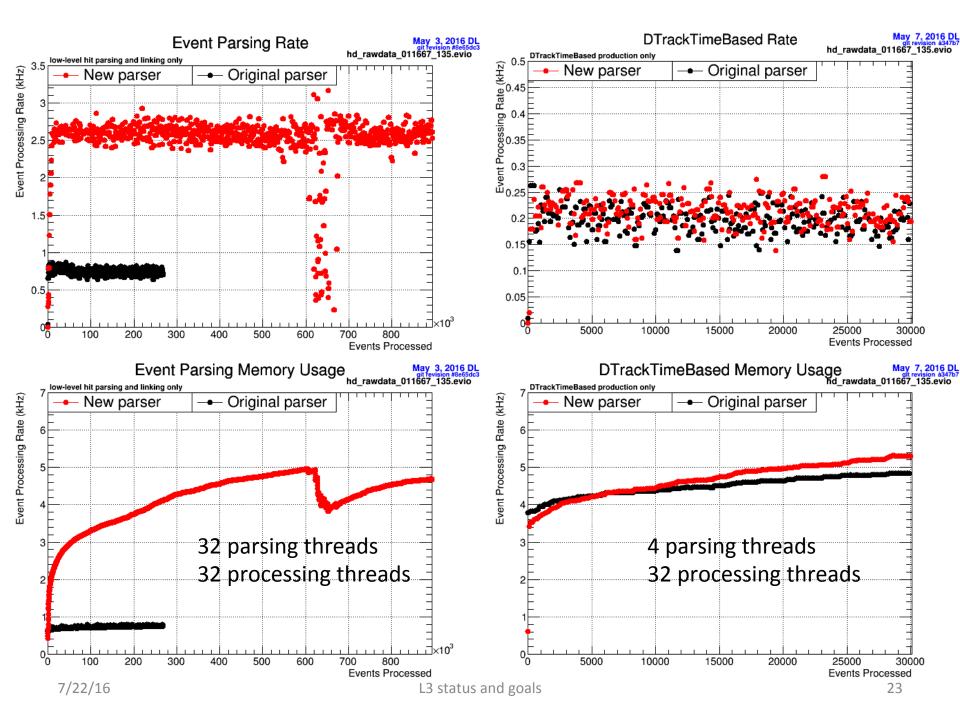
(no linking, no reconstruction)

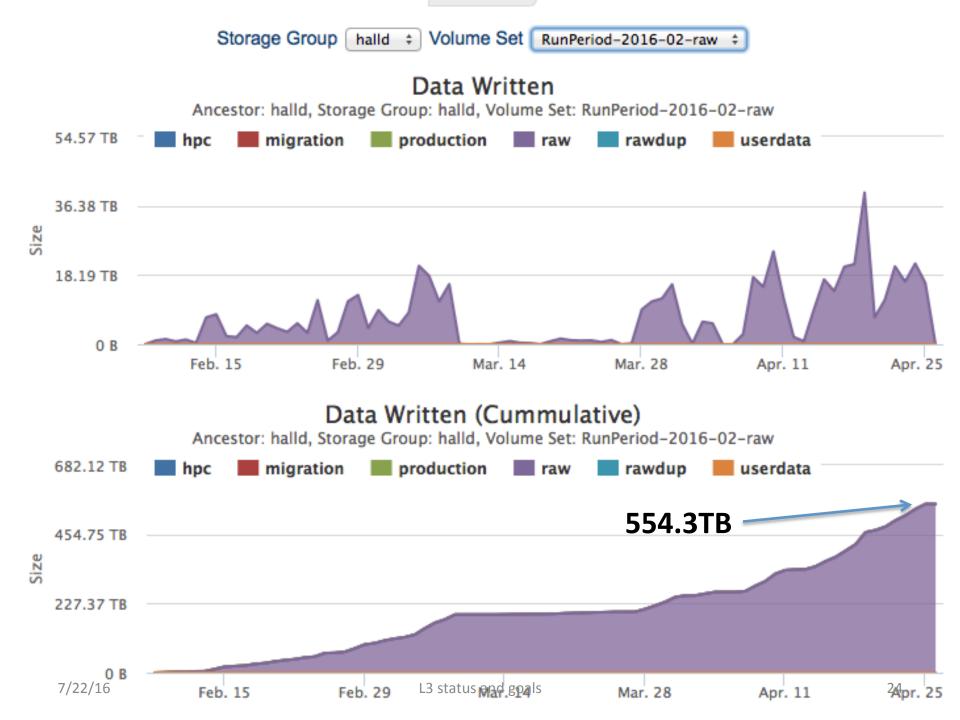
Steady state rate



New Parser Processing Rates May 7, 2016 DL hd_rawdata_011667_135.evio



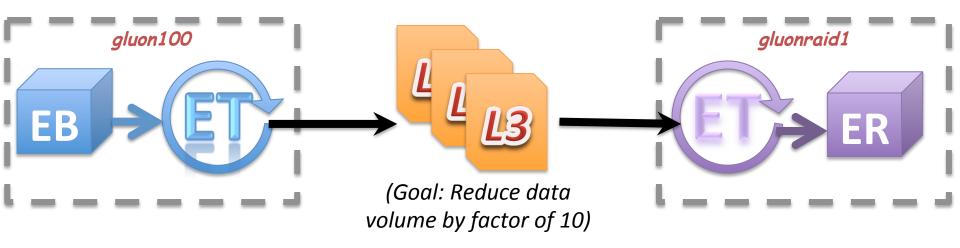




Online Storage Capacity

- Two RAID disks with 72TB each of usable space
 - Maintain some portion of recent data
 - ~100TB effective space total for new data
- Need 72hr buffer in case of issue with link to tape library
- $100TB \div 800MB/s = 35hr$
- Need additional 100TB of RAID
 - Will purchase this summer

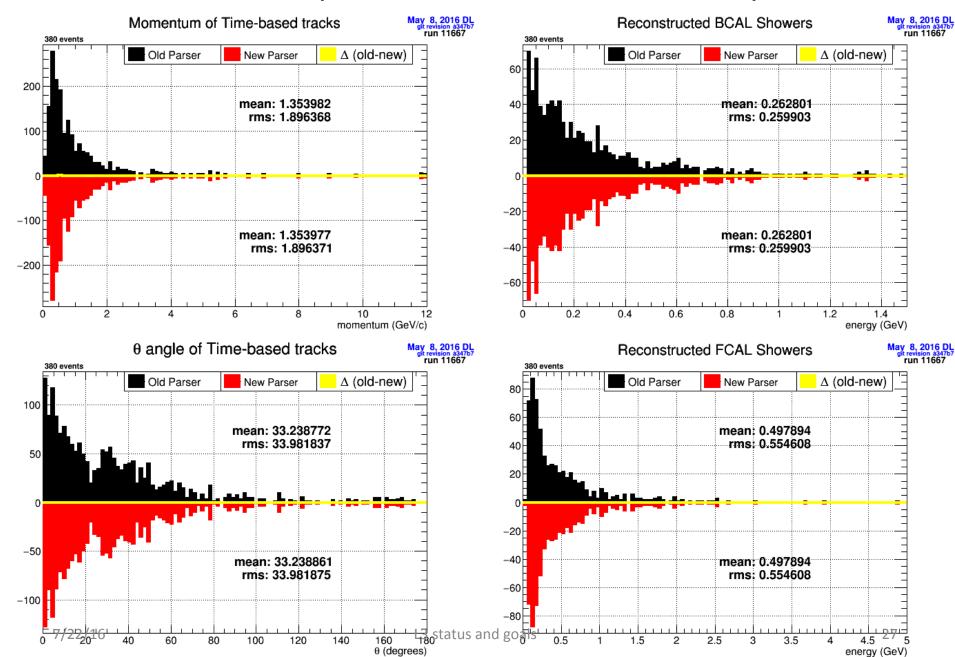
L3 Testing

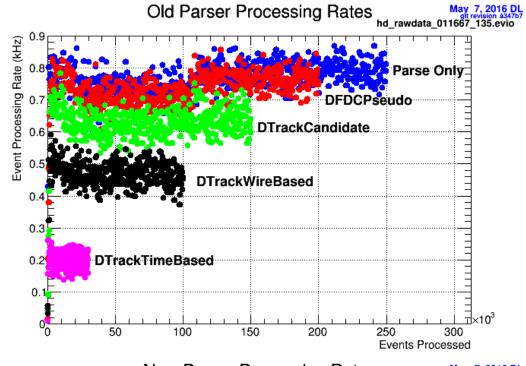


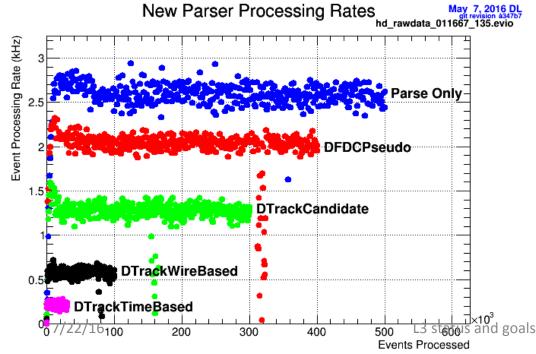
Spring 2016 L3 Testing

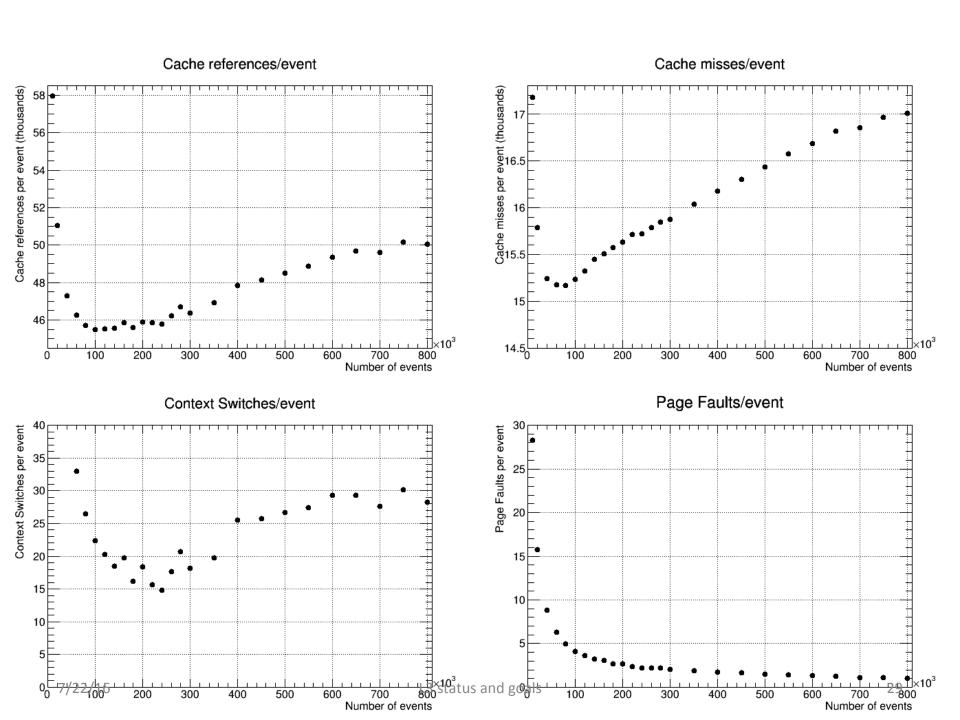
- Primarily infrastructure testing done
- Limited by 12 node farm to ~200MB/s parsing rate
- (~10kHz input rate)

Reconstructed parameters for old and new parsers









Input Test File

- hd_rawdata_011667_135.evio
 - 120nA, 50μm diamond (PERP), 5.0mm collimator
 - $-I_{\text{solenoid}} = 1345A$
 - 18kB/event
 - Measured I/O rate: ~900MB/s (=50kHz)
 - fspeed_reader
 - gluonraid2 -> gluon48
 - Maximum sim-recon read speed: ~33kHz
 - Parsing and linking disabled

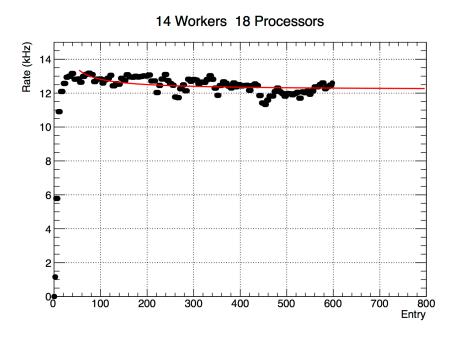
EVIO Parsing Time

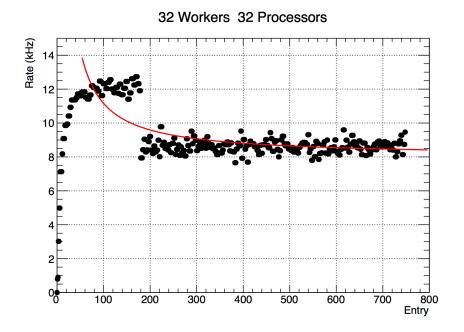
| Rate (kHz) | Time/core/event (ms) | Condition |
|------------|----------------------|--|
| 2.5 | 8.0 | All linking enabled |
| 2.9 | 6.9 | All linking except TriggerTime |
| 3.8 | 5.3 | All linking except BORConfig |
| 3.0 | 6.7 | All linking except Config |
| 4.8 | 4.2 | All linking except TriggerTime and BORConfig |
| 5.9 | 3.4 | Hit linking only |
| 8.0 | 2.5 | No Linking |

Fitting event rate vs. time

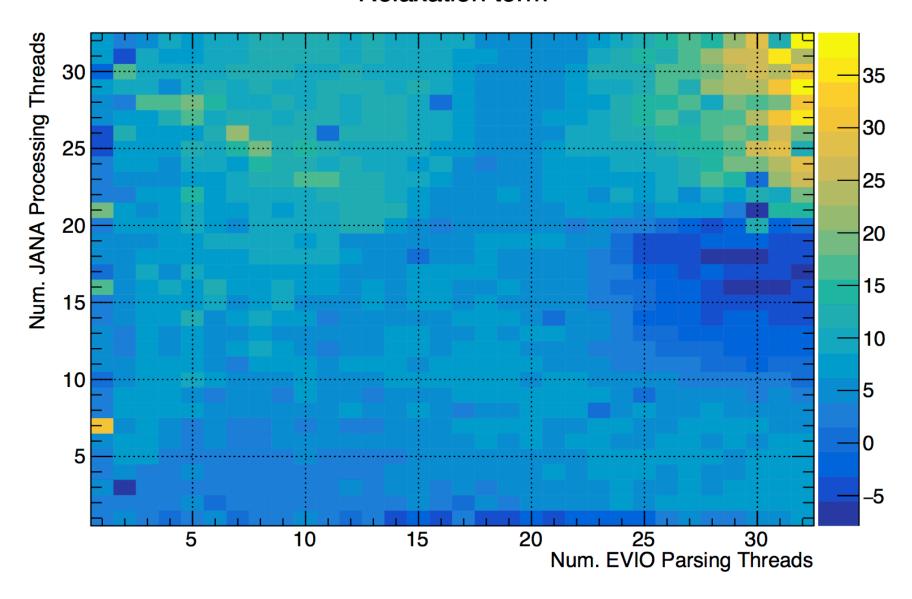
$$R(t) = R_o(1 + Q/t)$$

 R_o = asymptotic rate Q = relaxation term

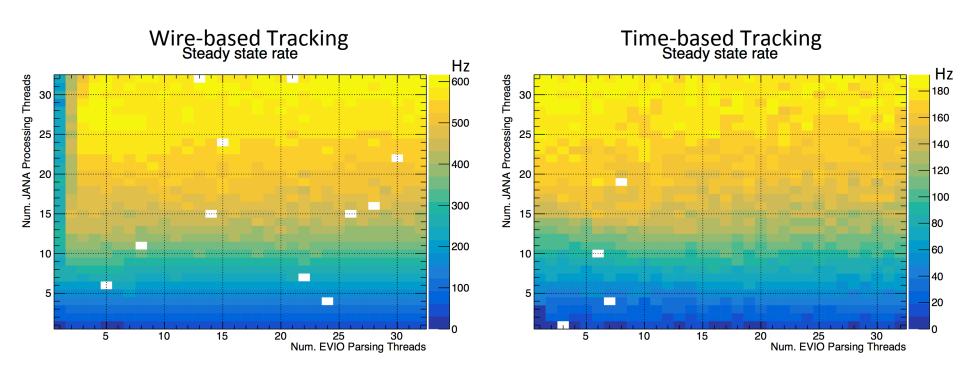




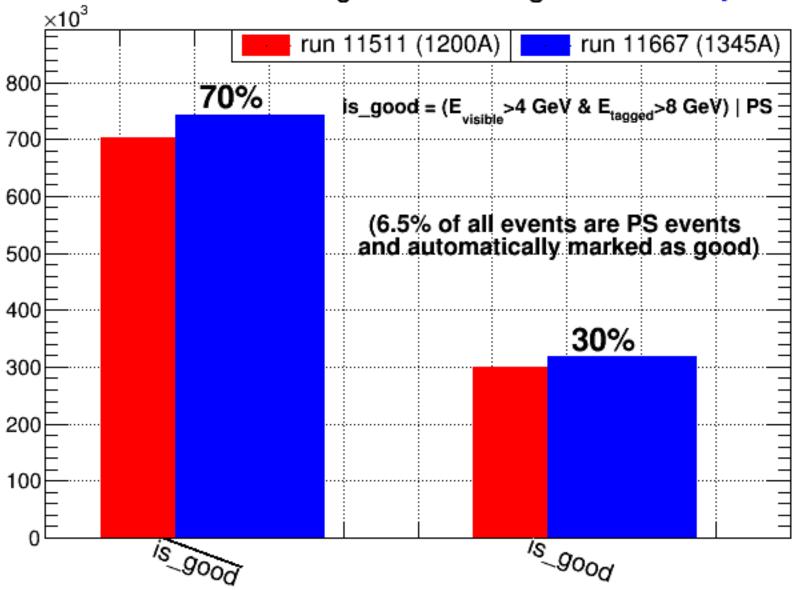
Relaxation term



Event rates with tracking

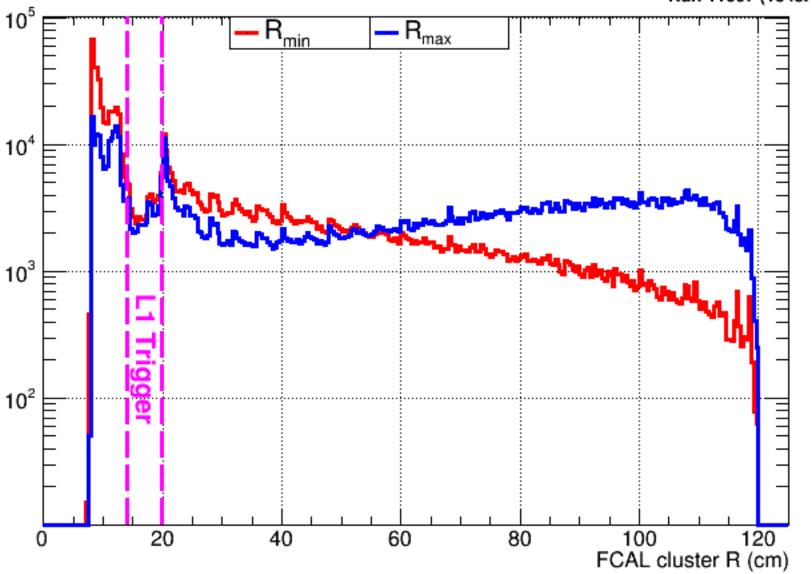


- With associated object linking, parsing threads run about 4 times slower
- Single parsing thread with full linking: ~250Hz

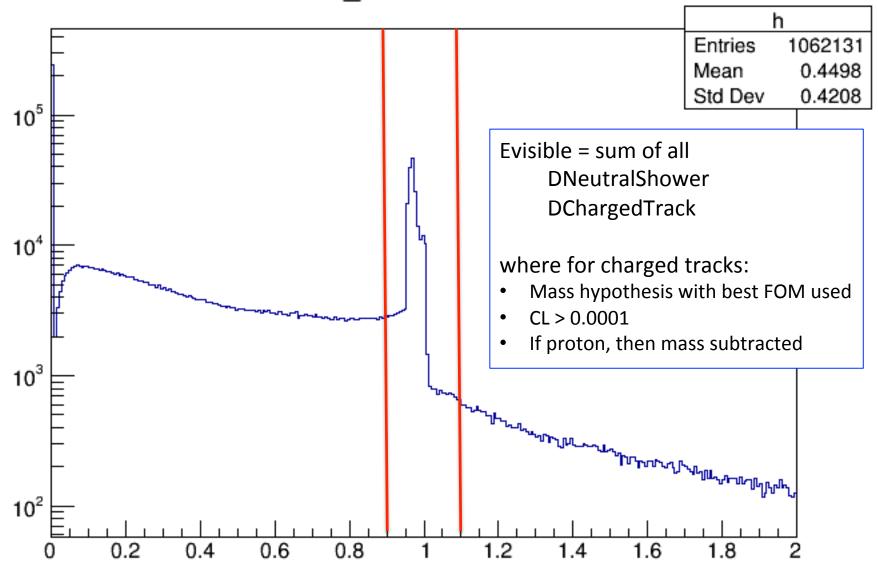


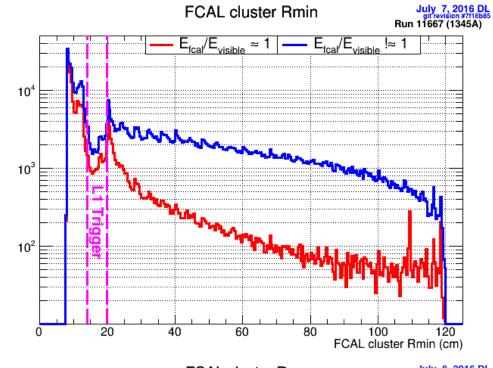


July 7, 2016 DL git revision #7f18b85 Run 11667 (1345A)



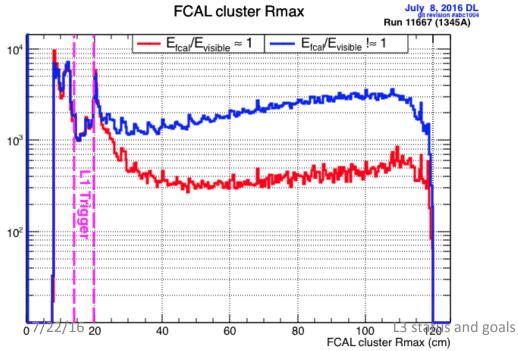
Efcal_clusters/Evisible



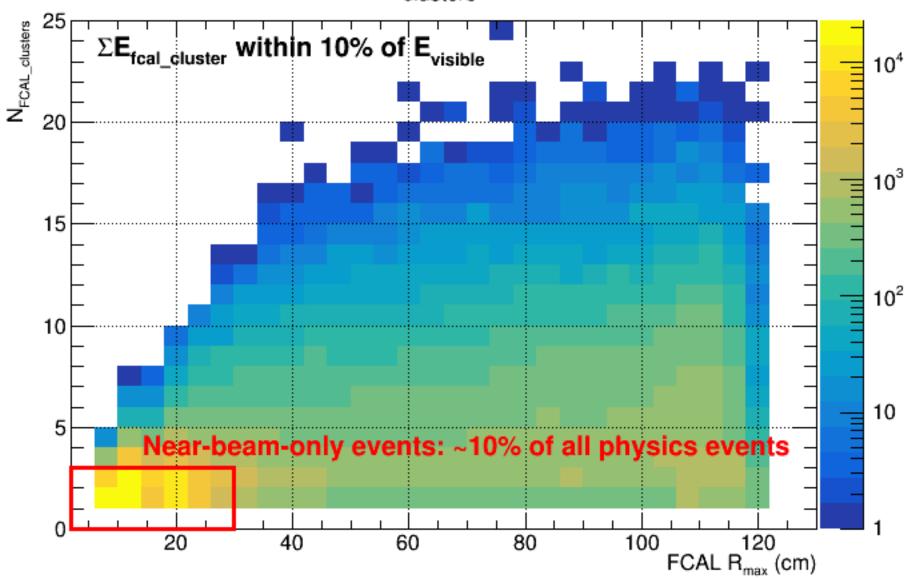


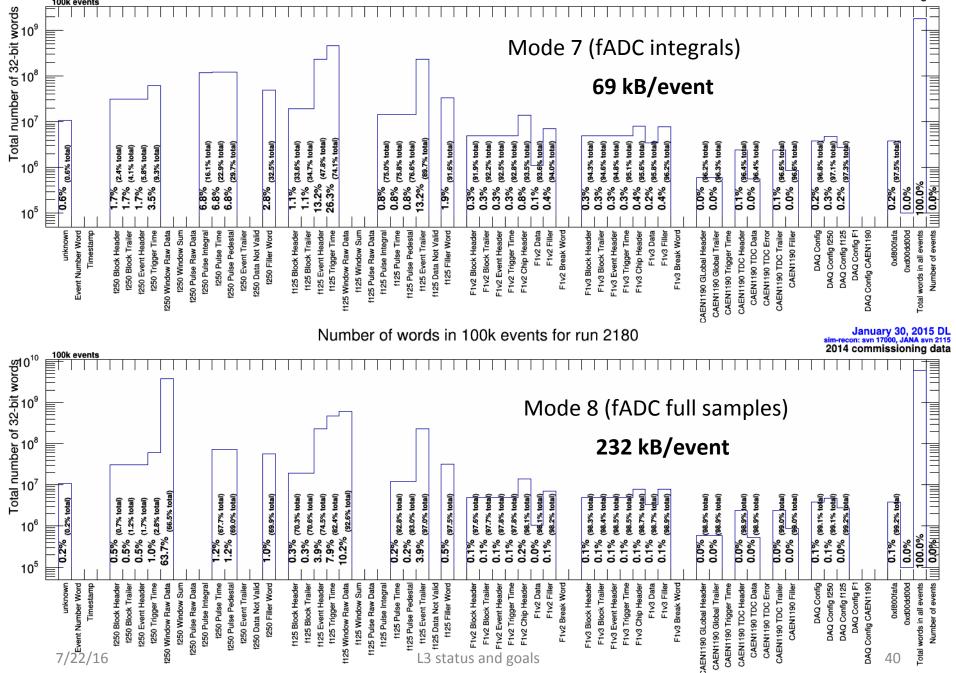
FCAL Rmin and Rmax

for when most of Evisible is inside (outside) of FCAL

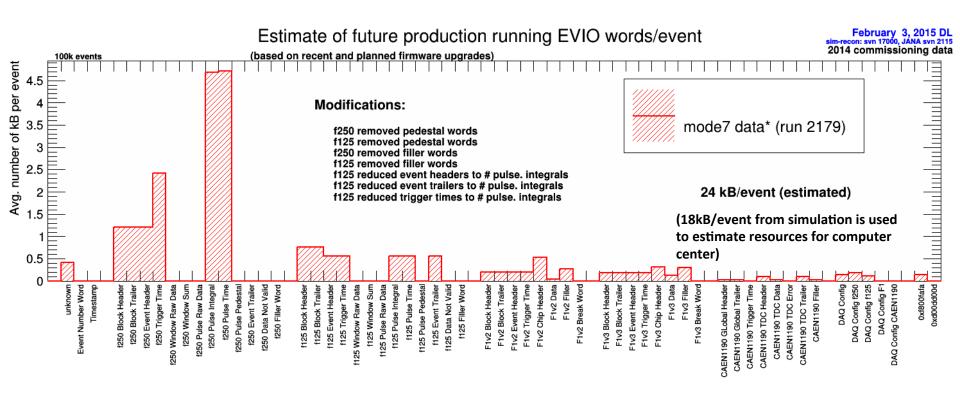








Adjusting profile of 2014 commissioning data based on recent or planned firmware upgrades is used to estimate event size for production data in the future.



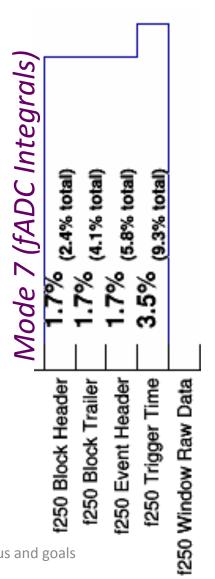
(Additional compression is expected when disentangled data is rebuilt after L3 into an as yet undetermined format.)

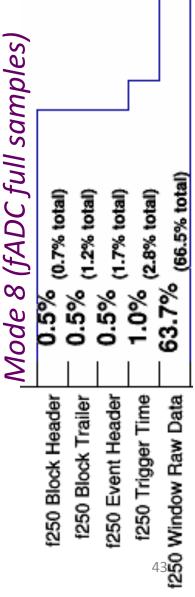
Counting house computer systems

| Computer(s) | processor | General Purpose Network | DAQ Network | I.B. Network | comments |
|--------------|-----------------------------|-------------------------------|---------------------------|-----------------|---|
| gluonfs1 | N/A | Χ | | | ~1.6TB with snapshot backup |
| gluonraid1-2 | Intel E5-2630 v2 @2.6GHz | X | X | X | RAID disk host ER process |
| gluon01-05 | i5-3570 @3.4GHz | X | | | Shift taker consoles |
| gluon20-23 | AMD 2347 | X | | | Controls 8core |
| gluon24-30 | E5-2420 @1.9GHz | X | | | Controls (gluon24 is web/DB/cMsg server) 12core + 12ht |
| gluon40-43 | AMD 6380 | X | Χ | X | 16core + 16"ht" |
| gluon46-49 | E5-2650 v2 @2.6GHz | Х | X (gluon47 &49) | Х | 16core + 16ht |
| gluon100-111 | E5-2650 v2 @2.6GHz | Χ | | Χ | 16core + 16ht |
| rocdev1 | Pentium 4 @2.8GHz | Х | | | RHEL5 system for compiling ROLs for DAQ |
| hdguest0-3 | i5-3470 @3.2GHz | X (outside network) | <u>L3 status an</u> | d-goals | Guest consoles in cubicles (outside network) |

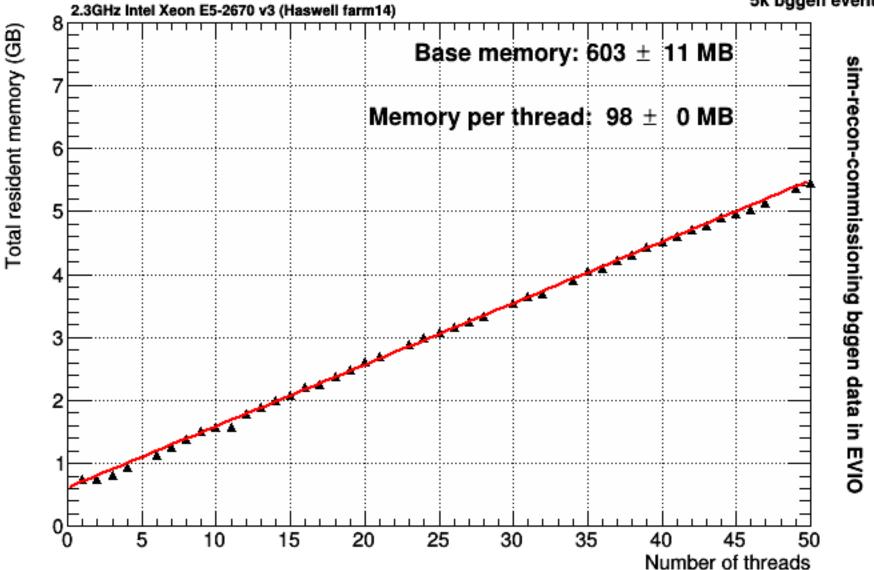
Example: some of the fADC250 word types

- Each 32bit word in the EVIO file tallied to identify what file space is being used for
- Comparison between mode 7 and mode 8 data made

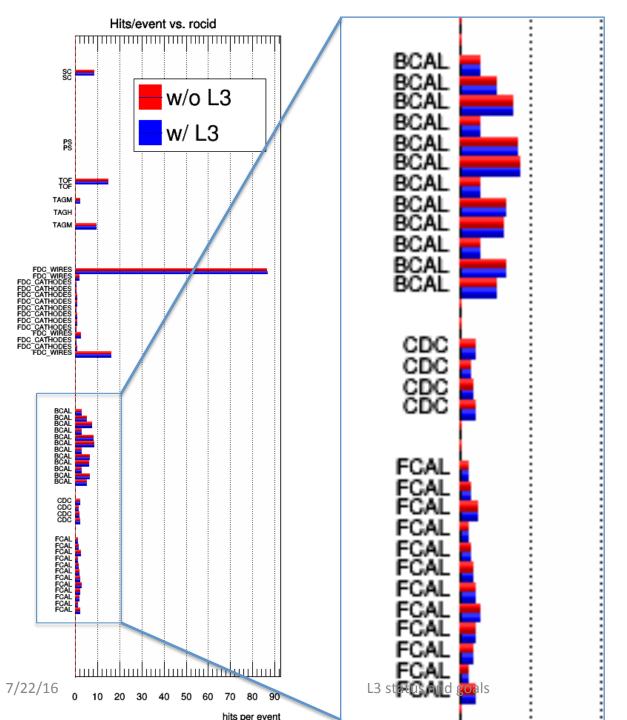




Memory Usage vs. Num. Threads January 23, 201



| name | node | level 🗸 | Nthr | Nevents | rate (Hz) | idle |
|-------------------------|-------------------|---------|------|---------|-----------|-------|
| gluon101.jlab.org_11057 | gluon101.jlab.org | | 32 | 212874 | 751.1 | 75.2% |
| gluon49.jlab.org_12748 | gluon49.jlab.org | | 32 | 206140 | 646.7 | 22.8% |
| gluon111.jlab.org_5726 | gluon111.jlab.org | | 32 | 206700 | 643.1 | 58.1% |
| gluon110.jlab.org_30100 | gluon110.jlab.org | | 32 | 208726 | 700.7 | 4.1% |
| gluon109.jlab.org_4409 | gluon109.jlab.org | | 32 | 213494 | 369.9 | 75.2% |
| gluon108.jlab.org_10935 | gluon108.jlab.org | | 32 | 247875 | 390.6 | 77.0% |
| gluon107.jlab.org_23963 | gluon107.jlab.org | | 32 | 211054 | 406.0 | 72.1% |
| gluon106.jlab.org_20172 | gluon106.jlab.org | | 32 | 219621 | 895.5 | 49.8% |
| gluon105.jlab.org_10192 | gluon105.jlab.org | | 32 | 212474 | 507.2 | 71.1% |
| gluon104.jlab.org_23134 | gluon104.jlab.org | | 32 | 205814 | 549.7 | 66.7% |
| gluon102.jlab.org_22561 | gluon102.jlab.org | | 32 | 206803 | 451.5 | 71.7% |
| gluon48.jlab.org_18860 | gluon48.jlab.org | | 32 | 214251 | 519.2 | 49.7% |
| | | | | | | |



Comparing number of hits before and after rewriting with L3