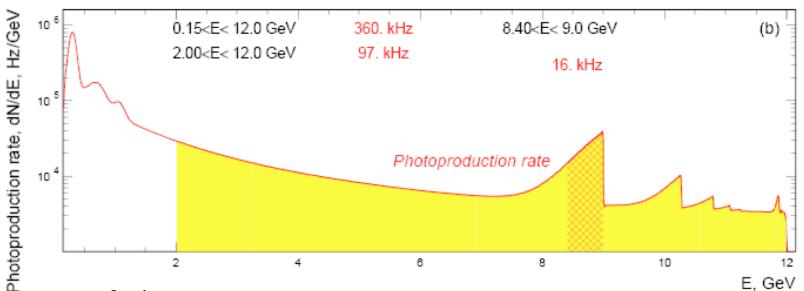
#### Hall-D L3 trigger status



### David Lawrence JLab July 22, 2016

#### From PR12-13-003 (GlueX strangeness proposal)

... we propose a gradual increase in the photon flux towards the GlueX design of  $10^8 \text{ y/s}$  in the peak of the coherent bremsstrahlung spectrum (8.4 GeV < E $\gamma$  < 9.0 GeV). Yield estimates, assuming an average flux of 5 × 10<sup>7</sup>  $\gamma$ /s, are presented.



- $10^8 \gamma$ /s on LH<sub>2</sub> target -> ~400kHz hadronic rate
- L1 trigger goal is to cut away ~50% leaving 200kHz
- L3 trigger goal is to reduce by ~90% leaving 20kHz actual: ~75%
- Early simulation suggested ~15kB/event actual: 14kB + 0.05kB/nA\*
  - 15kB/event @ 200 kHz = 3000 MB/s (front end)
  - L3 reduction by factor of 10 = 300MB/s to RAID disk

L1 trigger rates Fall 2014: 2kHz *Spring 2015: 3.5kHz* Spring 2016: 30kHz

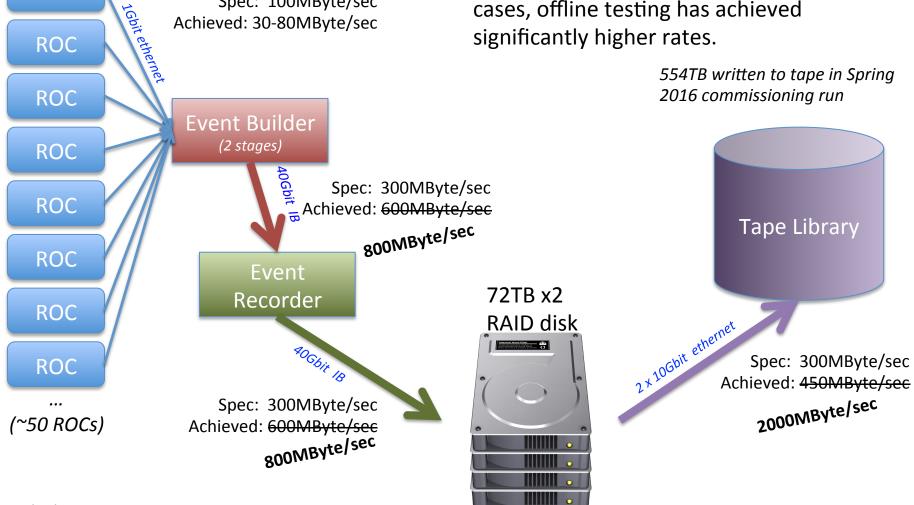
ROC

## Data Rates

Spec: 100MByte/sec

Achieved: 30-80MByte/sec

"Achieved" means with actual data while it was being acquired. In some cases, offline testing has achieved significantly higher rates.



L3 status and goals

L1 trigger rates Fall 2014: 2kHz *Spring 2015: 3.5kHz* Spring 2016: 30kHz

ROC

ROC

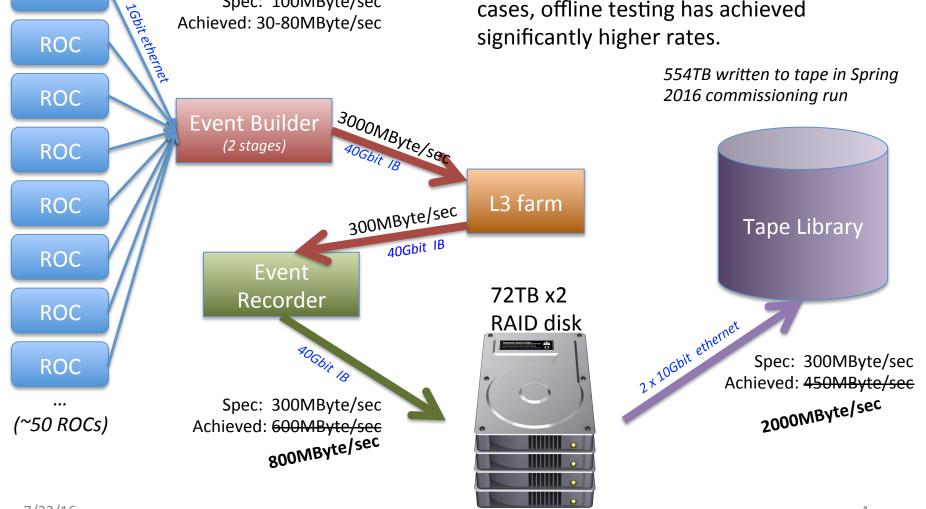
### Data Rates

Spec: 100MByte/sec

Achieved: 30-80MByte/sec

"Achieved" means with actual data while it was being acquired. In some cases, offline testing has achieved significantly higher rates.

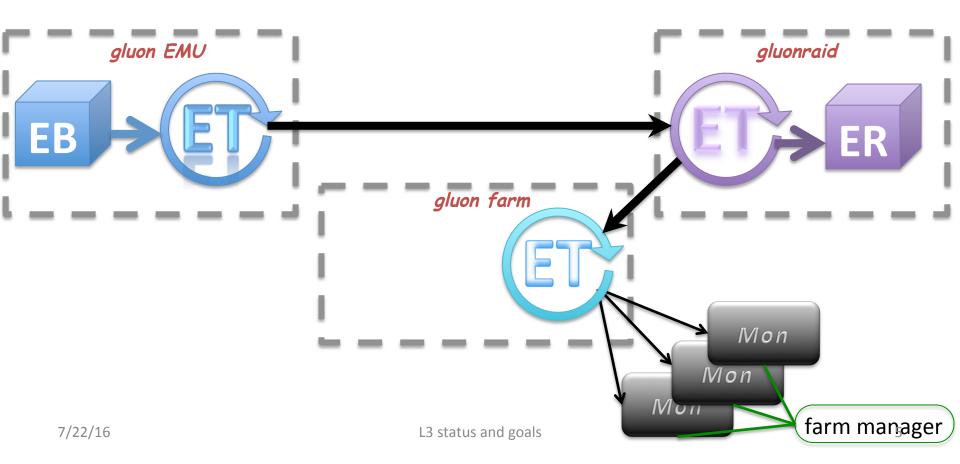
554TB written to tape in Spring



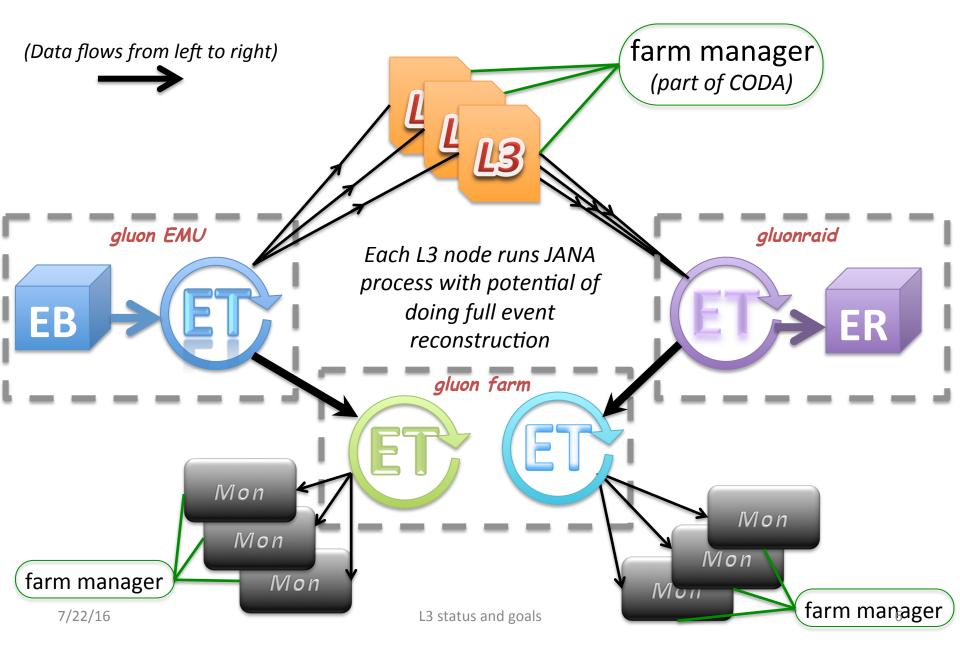
#### L3 and monitoring architecture

(Data flows from left to right)

#### Spring 2016 Configuration

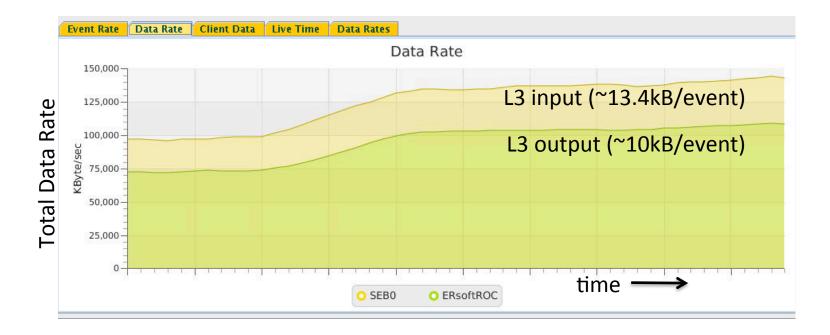


#### L3 and monitoring architecture

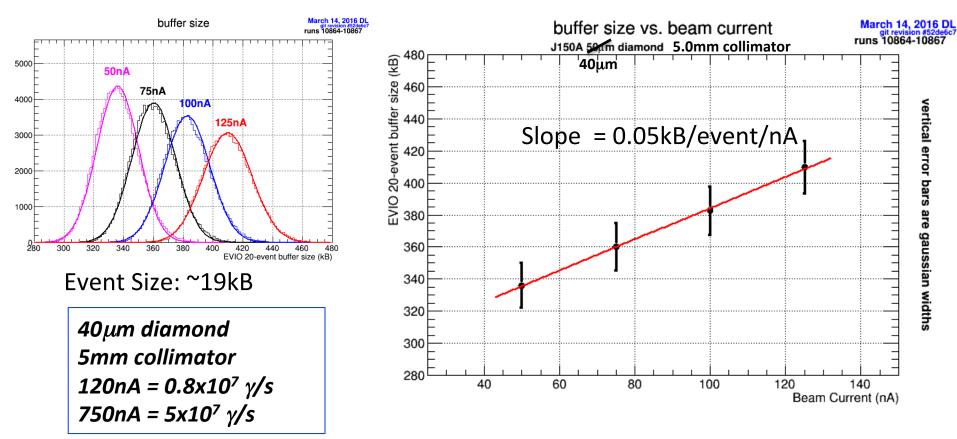


#### L3 running in pass-through with beam

- Events are read in blocks of 20
- Events must be disentangled and reconstituted as single events before writing to disk
- Redundant headers may be dropped to reduce event size



### Event Size vs. Beam Current

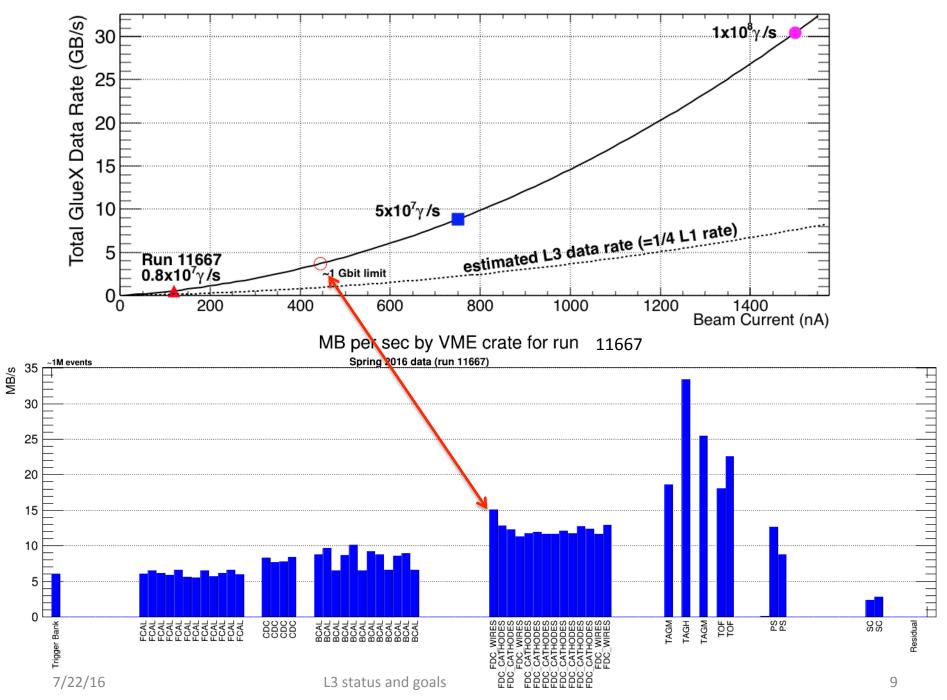


extrapolate to  $I_{beam} = 0 \rightarrow 14.25$  kB/event (size of clean event with no accidentals) Accidental data fraction (by volume): 100nA

 $(0.05 \text{kB/nA})(I_{\text{beam}} \text{ nA})$ (0.05 kB/nA)(I<sub>beam</sub> nA) + (14.25 kB) 100nA: 26% of data is due to accidentals200nA: 41% of data is due to accidentals750nA: 72% of data is due to accidentals

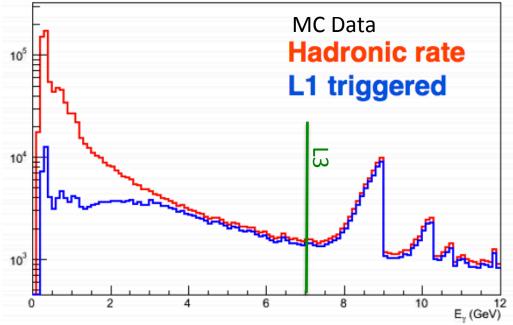
Estimated Data Rate vs. Beam Current

July 21, 2016 DL git revision #abc1004



# L3 Algorithm Strategy

- Use Multivariate Analysis such as BDT or ANN to classify events (LHCb)
- Multiple levels may be used with each level requiring more expensive input variables



- Use Multivariate Analysis such as BDT or ANN to classify events
- Multiple levels may be used with each level requiring more expensive input variables
- Use fully reconstructed, real data to provide training samples (signal and background)
- Simultaneously pursue with simulated data

## **Reconstruction times survey**

Time is divided by #calls and #threads

J1A50 50 um radiator, PERP, 7 mode, 105 nA beam cur rent, 27 kHz event rate, live time ~70% , LH2 fill, 5 mm collimator, 83 M total events

input file:	phys_skim.evio			
events	ALGORITHM	INPUT OBJECT	RECO TIME [s]	RECO TIME/event [ms]
10000	nominal reco	DNeutralShower	4.410	0.028
DATA	approx reco	DBCALShower	2.750	0.017
	approx reco	DFCALShower	8.010	0.050
	full tracking	DTrackTimeBased	18669.810	116.69
	approx tracking	DTrackWireBased	7397.300	46.23

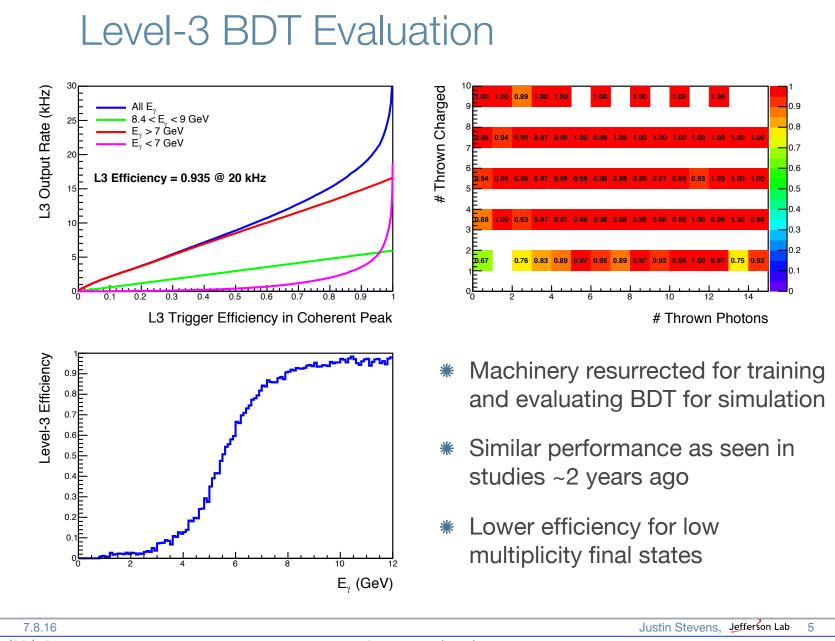
hdgeant\_smeared\_14980

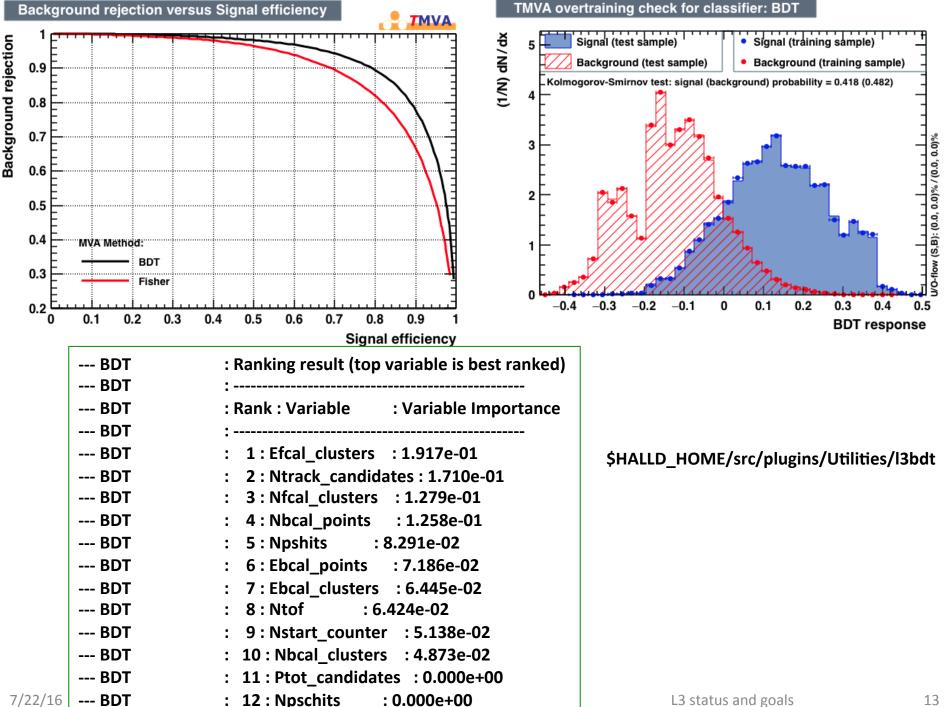
hd rawdata 010913 060.

events	ALGORITHM	INPUT OBJECT	RECO TIME [s]	RECO TIME/event [ms]
10000	nominal reco	DNeutralShower	5.730	0.036
МС	approx reco	DBCALShower	3.430	0.021
	approx reco	DFCALShower	7.010	0.044
	full tracking	DTrackTimeBased	23878.840	149.243
	approx tracking	DTrackWireBased	12778.340	79.865

#### n.b. parsing of evio data takes 0.080 – 0.400 ms/event

6





# Estimated number of L3 nodes

- From Spring 2016 running:  $\sim$ 30kHz/0.8x10<sup>7</sup>  $\gamma$ /s
- For 5x10<sup>7</sup> γ/s: ~190kHz
- 2013 Ivy Bridge nodes
  - 2.5-13kHz parsing only
  - 4kHz parsing+neutrals recon
  - 2kHz final algorithm (rough estimate)
- Newer nodes assume x2 faster – 4kHz/node
- Total number of nodes required:
  - 190kHz/4kHz = **48**



## Schedule



- Continue L3 development, testing infrastructure and MVA efficiency through Fall
- Integrate testing schedule into Fall 2016 run
- If confident, take portion of Spring 2017 data in pass-through mode
  - Events on tape will be reconstituted single events
  - Event tagging may be used if fast algorithm available
- Full L3 deployment Fall 2018

# **Potential Issues**

And And

- fADC125 high rate performance
  - Busy signals (currently being tested in Hall-D)
- Individual VME crate saturates 1Gbit link
  - 10Gbit ethernet cards in ROCS
  - Split crates (either backplanes or add additional crates)
  - Parallel module readout (VXS, or new fADC125 daughters)
- 50 ROCs pushing 9GB/s through full CODA system
  - Minimize single crate data size to allow high luminosity testing
  - May require tuning IB network or splitting streams

### **Backup Slides**

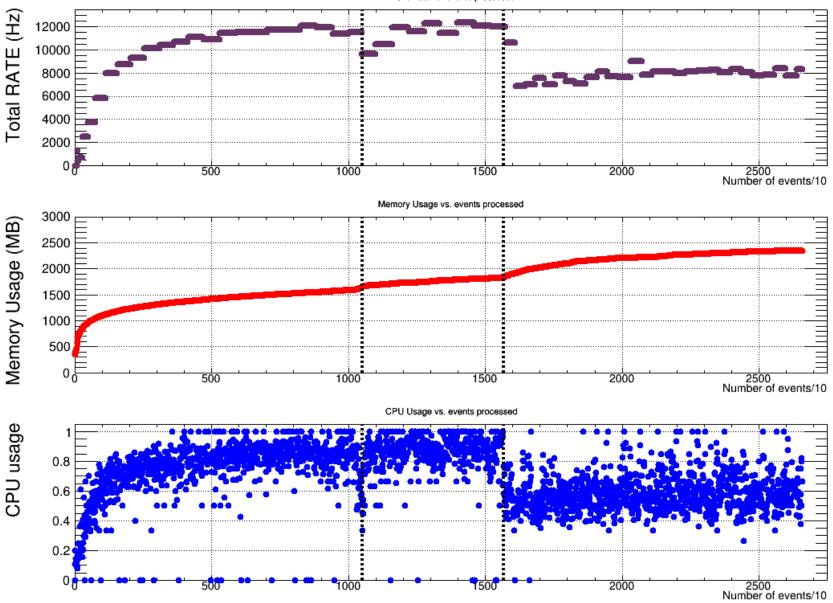


## **Detectors At High Rate**

- FDC aging effects for wires closest to beamline
- FCAL innermost blocks radiation damage
  - Curing
  - Replace with rad-hard insert

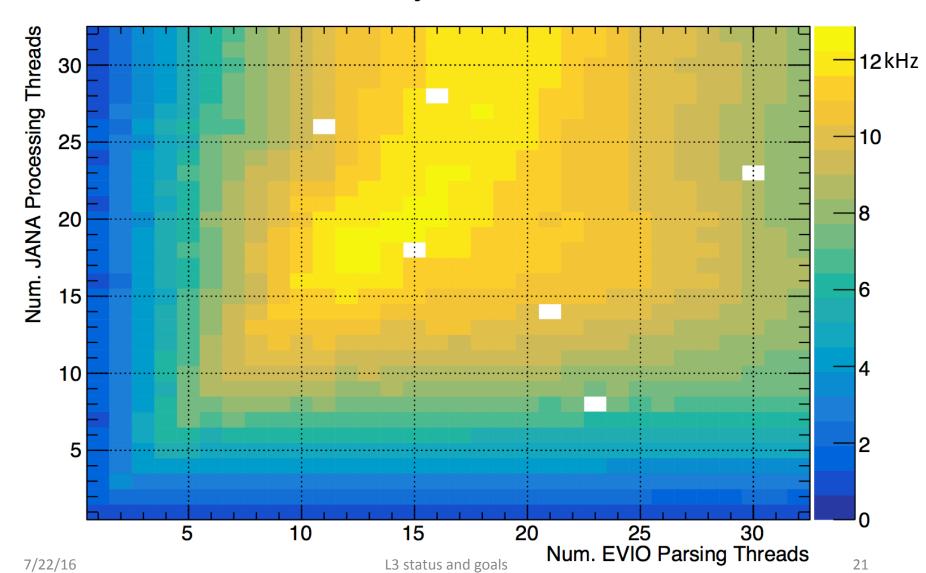
- Potential concern for the CDC and FDC is the aging of the wires: high currents in the chambers which, especially in presence of organic contamination, can result in formation of deposits on the wire surface (like polymers) and manifest as a decrease of the gas gain.
- The relevant quantity is the charge per unit wire length. For the nominal low intensity during the commissioning/ engineering running we had in the inner FDC HV sector (20 wires) about 5 uA currents, which however is concentrated to the area closest to the beam line, where we expect to have up to 1 uA/cm. In the CDC the currents on 24 inner straws was about 10 uA, but distributed evenly along each wire and between the 24 wires, corresponding to current densities of about 5 nA/cm.
- No wire aging effects at all have been observed in Ar/CO2 gas mixtures in studies with up to 1C/cm, which however corresponds to only ~12 beam-on-target days at low intensity. Up to now (??total charge/cm??) we have not seen any wire aging. Even wire aging is possible at higher intensities, it will be concentrated in a small area around the beam line.
- Conclusions: don't expect problems with the CDC; in FDC aging is possible but only in a small area around the beam.
- In order to mitigate potential aging effects due to radiation we do add alcohol to the gas. This is proven to stop the progress of aging if already present and inhibit aging from happening in the first place. As an example HallC operated their wire chambers for more than a decade now using alcohol in the gas mix and to my knowledge no aging has been observed. (This statement should be confirmed by Howard Fenker)
- One potential issue could be radiation damage to the FCAL lead glasses over the prolonged period of highluminosity running.

Event Rate vs. events processed

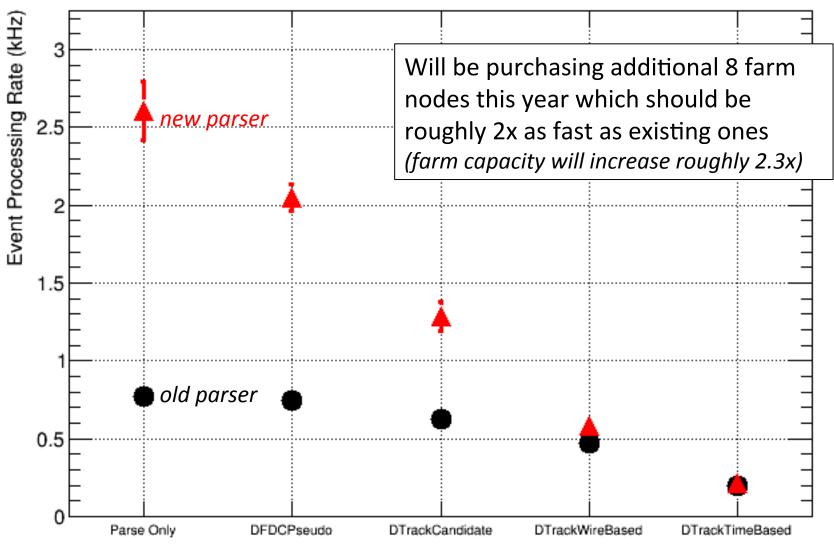


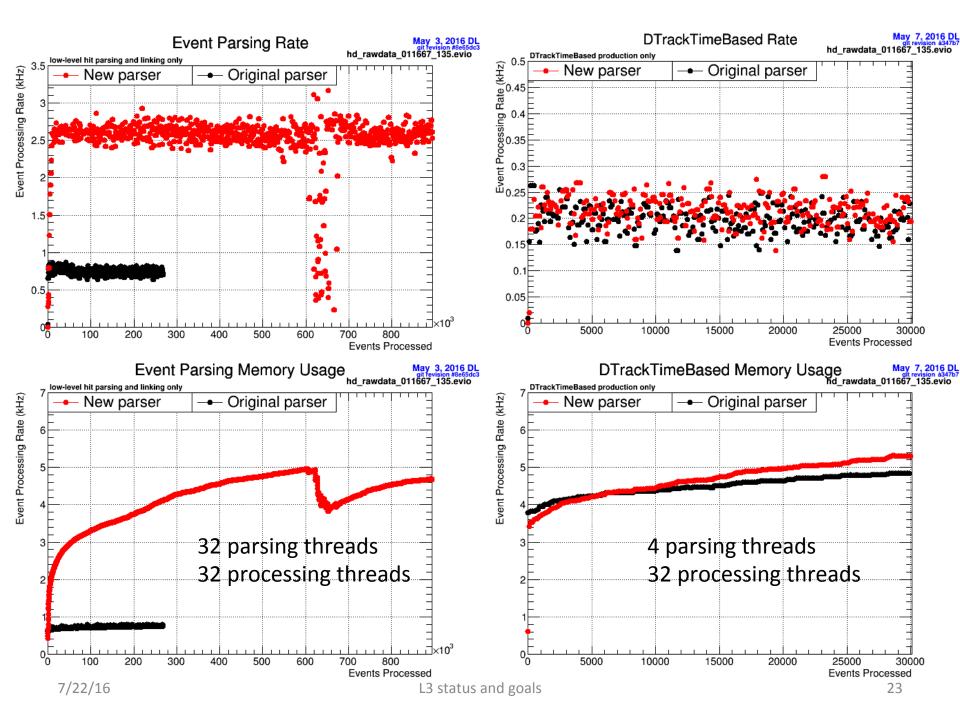
## Parsing only

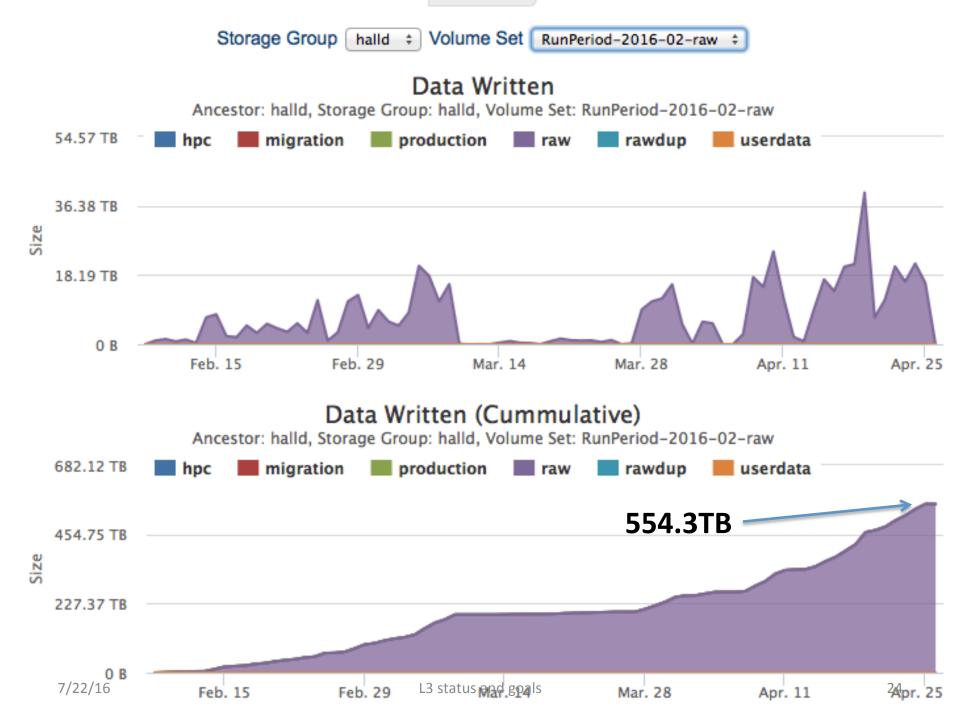
(no linking, no reconstruction) Steady state rate



#### New Parser Processing Rates May 7, 2016 DL hd\_rawdata\_011667\_135.evio







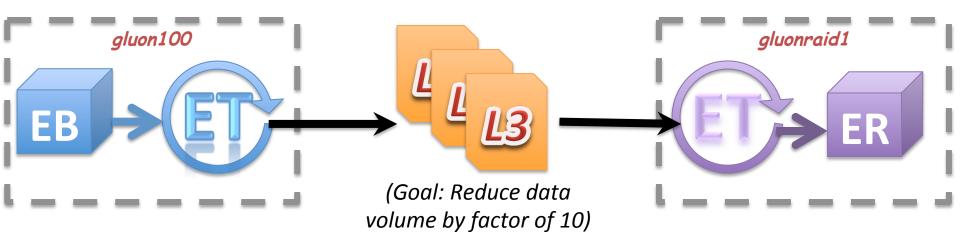
# **Online Storage Capacity**

- Two RAID disks with 72TB each of usable space
  - Maintain some portion of recent data
  - ~100TB effective space total for new data
- Need 72hr buffer in case of issue with link to tape library
- 100TB ÷ 800MB/s = 35hr
- Need additional 100TB of RAID

- Will purchase this summer



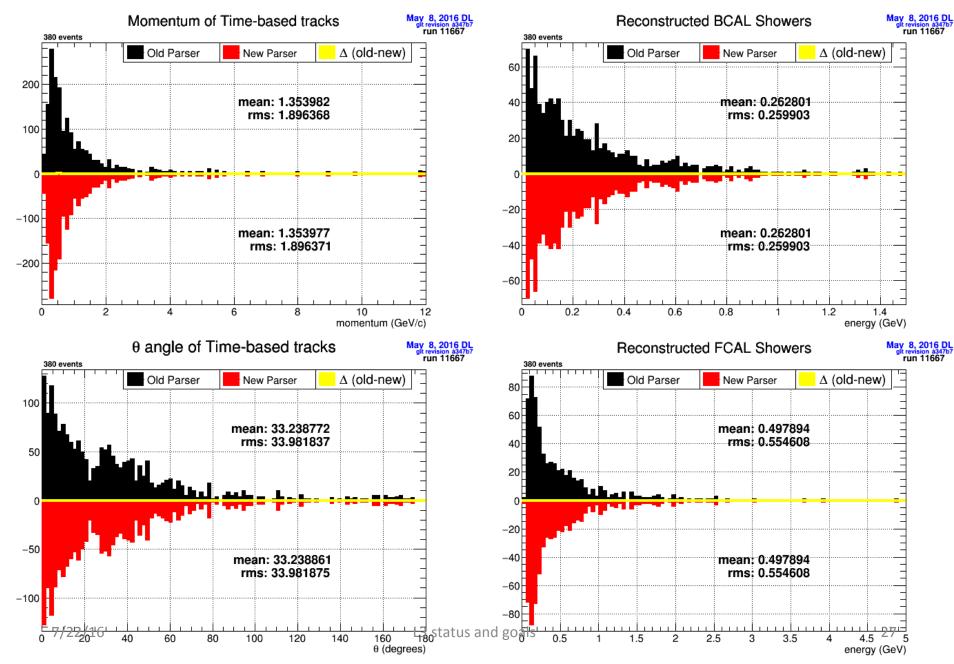
#### L3 Testing

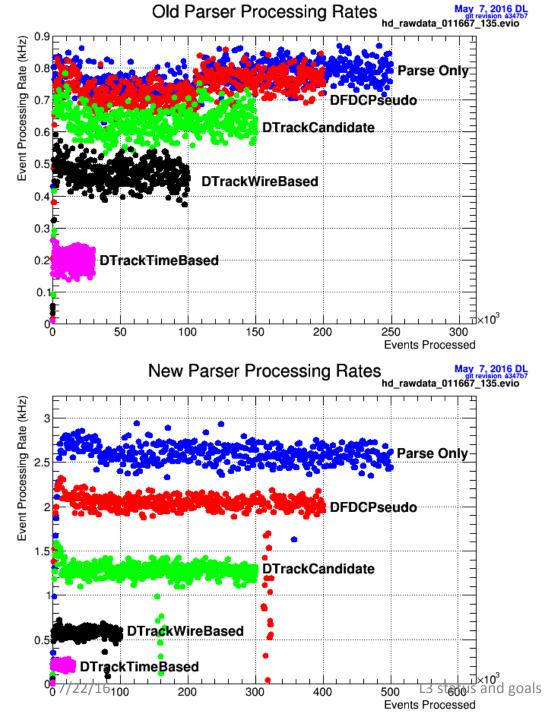


#### Spring 2016 L3 Testing

- Primarily infrastructure testing done
- Limited by 12 node farm to ~200MB/s parsing rate
- (~10kHz input rate)

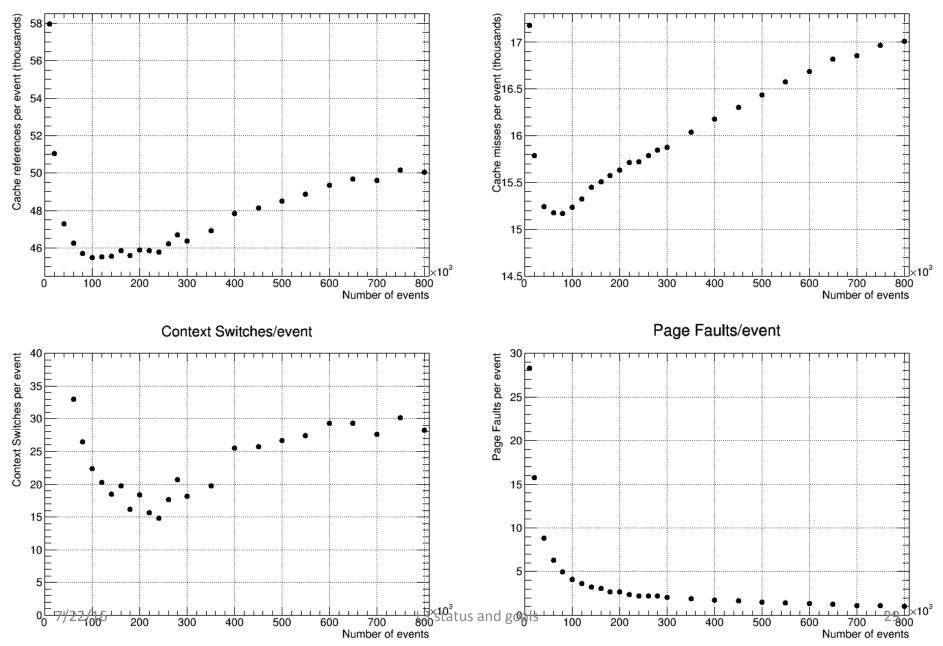
#### Reconstructed parameters for old and new parsers





Cache references/event

Cache misses/event

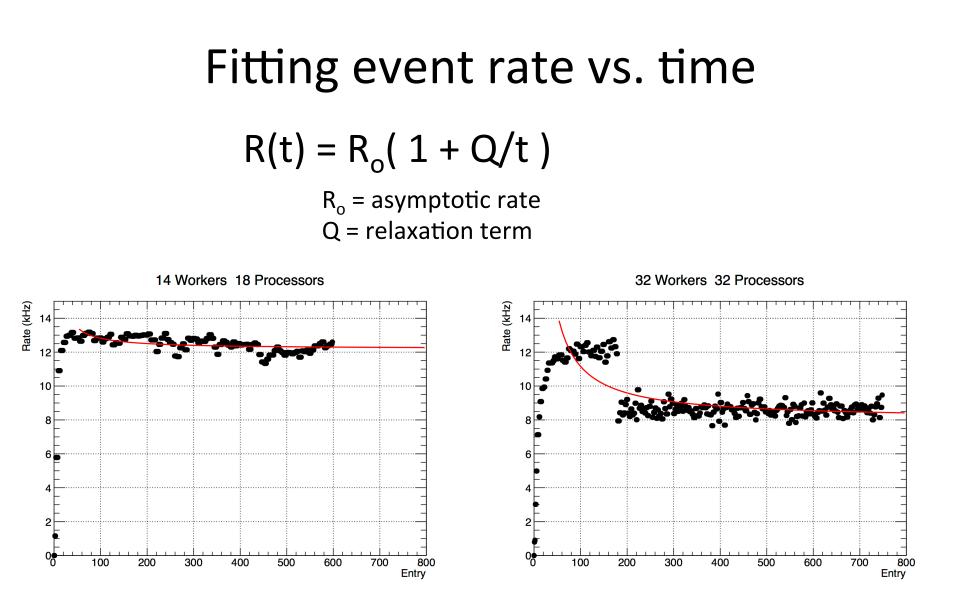


## Input Test File

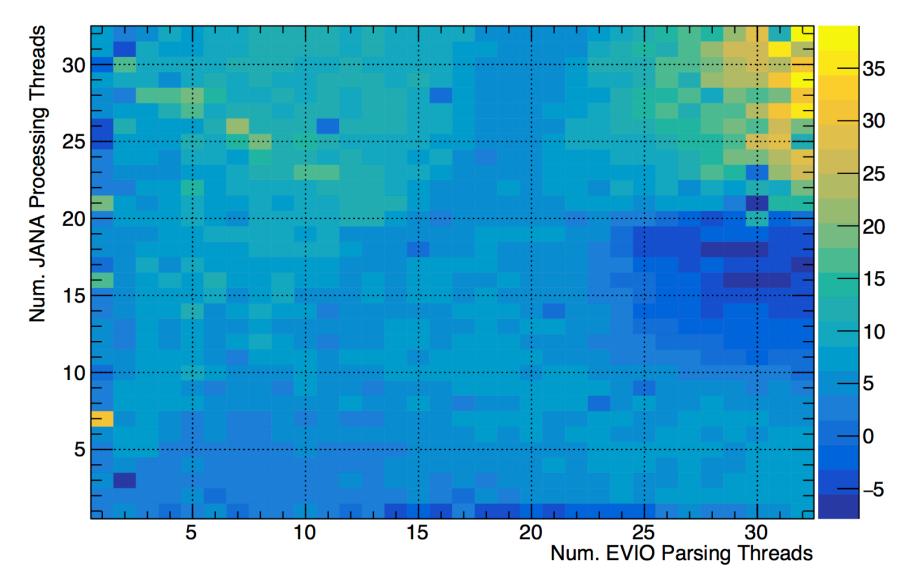
- hd\_rawdata\_011667\_135.evio
  - 120nA, 50µm diamond (PERP), 5.0mm collimator
  - $-I_{solenoid} = 1345A$
  - 18kB/event
  - Measured I/O rate: ~900MB/s (=50kHz)
    - fspeed\_reader
    - gluonraid2 -> gluon48
  - Maximum sim-recon read speed: ~33kHz
    - Parsing and linking disabled

# **EVIO** Parsing Time

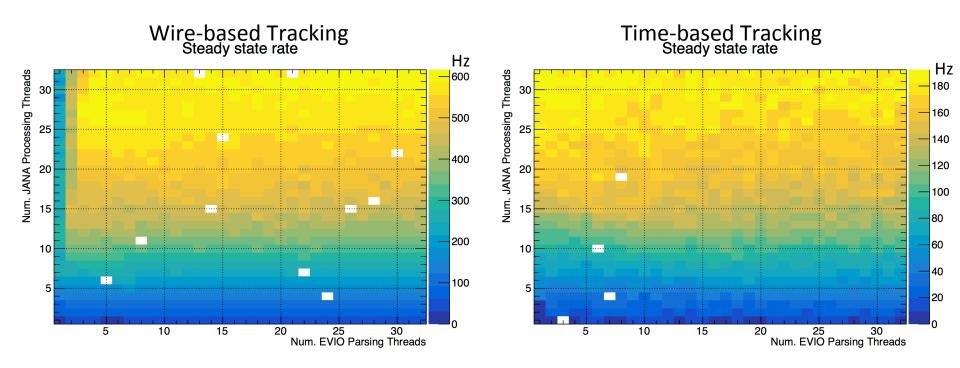
Rate (kHz)	Time/core/event (ms)	Condition
2.5	8.0	All linking enabled
2.9	6.9	All linking except TriggerTime
3.8	5.3	All linking except BORConfig
3.0	6.7	All linking except Config
4.8	4.2	All linking except TriggerTime and BORConfig
5.9	3.4	Hit linking only
8.0	2.5	No Linking



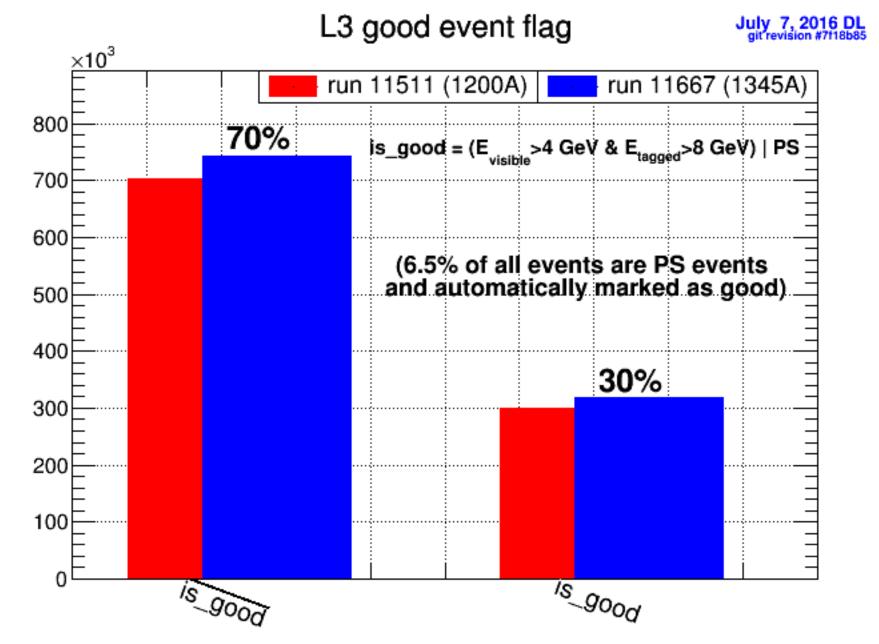
#### **Relaxation term**



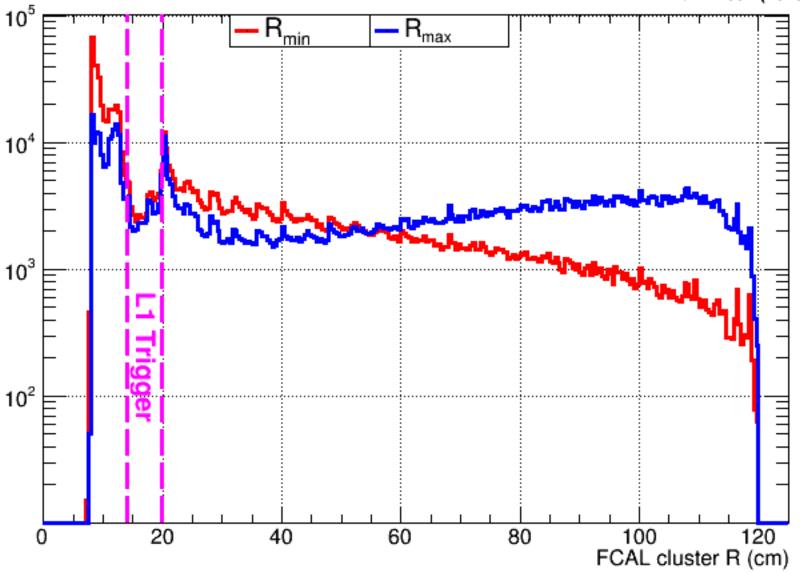
## Event rates with tracking



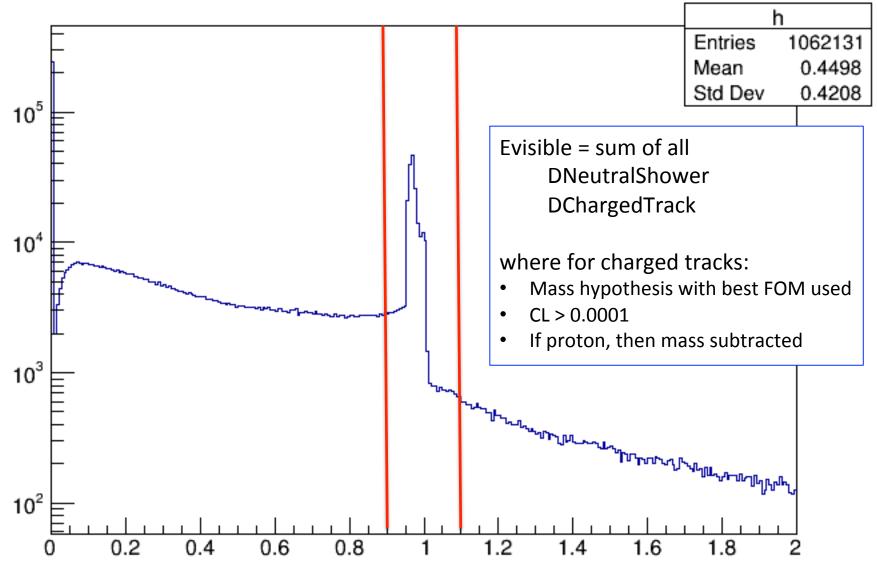
- With associated object linking, parsing threads run about 4 times slower
- Single parsing thread with full linking: ~250Hz

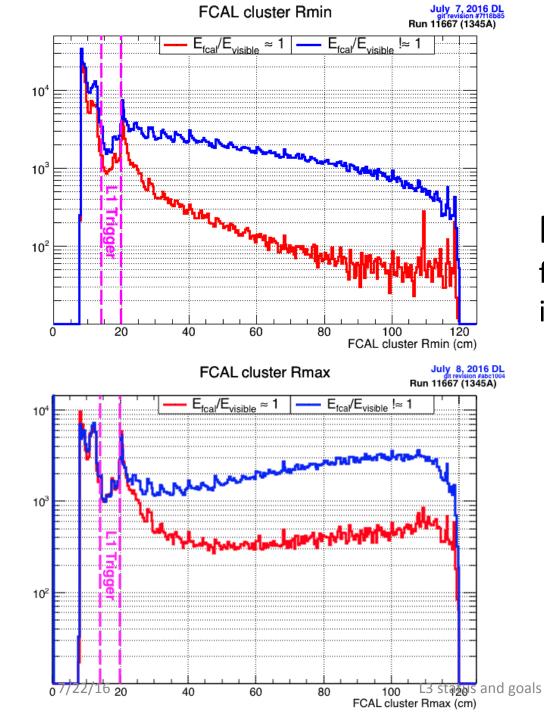


FCAL cluster R



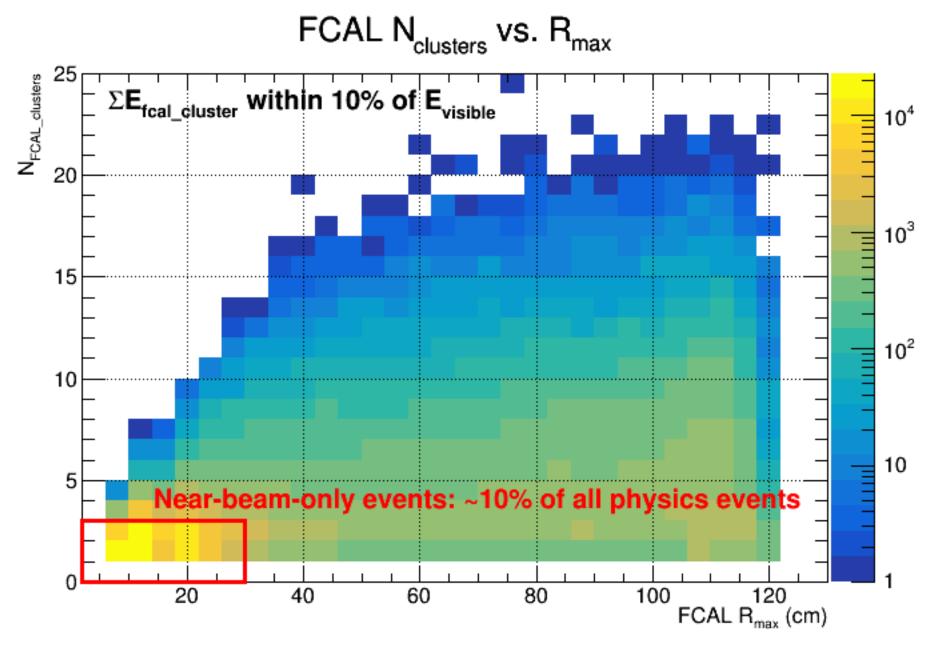
#### Efcal\_clusters/Evisible





#### **FCAL Rmin and Rmax** for when most of Evisible is

inside (outside) of FCAL



Number of words in 100k events for run 2179

100k events

total

0.6% (0.6%) 

> unknown Event Number Word

Timestamp

[250 Block Header f250 Event Header f250 Trigger Time f250 Window Raw Data

f250 Block Trailer

f250 Pulse Pedestal f250 Data Not Valid f250 Filler Word

f250 Pulse Time f250 Event Trailer

f250 Window Sum f250 Pulse Raw Data f250 Pulse Integral

Total number of 32-bit words

10<sup>6</sup>

10<sup>5</sup> 

January 30, 2015 DL sim-recon: svn 17000, JANA svn 2115 2014 commissioning data Mode 7 (fADC integrals) 69 kB/event 1.1% (34.7% total) 13.2% (47.8% total) 26.3% (74.1% total) (75.0% total) 0.8% (75.8% total) 0.8% 1.9% (91.6% total) 0.2% (96.8% total) 0.3% (97.1% total) 0.2% (97.3% total) 6.8% (16.1% total) 6.8% (22.9% total) 6.8% (22.9% total) 1.1% (33.6% total) 0.1% (96.6% total) 13.2% (89.7% total) 0.1% (<u>96.4% tot</u>al) 0.0% (<u>96.4% tot</u>al) (97.1% total) 0.2% (97.5% total) 2.8% (32.5% total) 0.8% (75.0% total) (76.6% total) (92.8% total) (95.1% total) 0.0% (96.2% total) (91.9% total) (92.2% total) (92.5% total) (93.5% total) (93.6% total) (94.0% total) (94.8% total) (96.3% total) (96.4% total) (96.6% total) (97.3% total) (29.7% total) (94.3% total) (94.6% total) (95.6% total (95.8% total (96.2% total) (5.8% total) (4.1% total) (9.3% total) (2.4% total) 1.7% 1.7% 3.5% 1.1% %0.0 0.0% 0.3% 0.3% 0.3% 0.3% 0.4% 0.2% 0.4% 0.3% 0.8% 0.1% 0.4% 0.3% 0.3% 0.3%

F1v2 Break Word

F1v3 Block Header

F1v3 Block Trailer F1v3 Event Headel

F1v2 Filler

F1v2 Data

F1v3 Break Word

CAEN1190 GLobal Header CAEN1190 Trigger Time CAEN1190 TDC Header

CAEN1190 Global Trailer

F1v3 Data F1v3 Filler

F1v3 Trigger Time F1v3 Chip Header CAEN1190 TDC Data CAEN1190 TDC Error CAEN1190 TDC Trailer

Number of words in 100k events for run 2180

F1v2 Block Header

F1v2 Block Trailer F1v2 Event Headel F1v2 Chip Headel

F1v2 Trigger Time

f125 Pulse Raw Data f125 Pulse Integral f125 Pulse Pedestal f125 Data Not Valid f125 Filler Word

f125 Pulse Time

f125 Event Trailer

f125 Trigger Time 125 Window Raw Data f125 Window Sum

f125 Block Header f125 Block Trailer f125 Event Header

January 30, 2015 DL sim-recon: svn 17000, JANA svn 2115 2014 commissioning data

0xf800fafa

Total words in all events

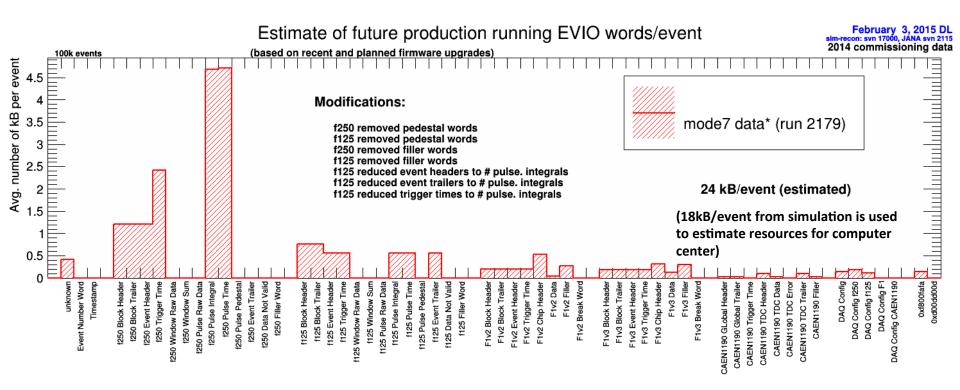
DAQ Config f125 DAQ Config F1 DAQ Config CAEN1190

DAQ Config DAQ Config f250

CAEN1190 Filler

100k events					2014 commissioning data
କ୍ଷା0" 📑 🗆 🗌					
al number of 32-bit wor		(total) (total) (total) (total) (total)		e 8 (fADC full samples) 232 kB/event	
	0.7% total) 1.2% total) 1.7% total) (66.5% total) (66.5% total) 67.7% total	9% to 3% to 5% to 1,6% to	8% total) .0% total) .0% total) .5% total) .8% total) .8% total) .1% total) .1% total) .1% total)		
10 <sup>6</sup>			(92.8% total) (97.0% total) (97.5% total) (97.5% total) (97.5% total) (97.8% total) (97.8% total) (98.1% total) (98.2% total) (98.2% total)	(98.7) (98.7) (98.7) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.9) (98.7) (97.7) (97.7) (97.7) (97.7) (97.7) (97.7) (97.7) (97.7) (97.7) (97.7) (97.7) (97.7) (97.7) (9	0.1% (99.1% toled) 0.1% (99.1% toled) 0.0% (99.2% toled) 0.0%
<b>%</b>	0.5% 0.5% 63.7% 1.2%	1.0% 0.3% 7.9% 10.2%	0.2% 0.2% 0.1% 0.1% 0.1% 0.1% 0.1% 0.1%	0.1% (se. 0.1% (se. 0.1% (se. 0.1% (se. 0.1% (se. 0.1% (se. 0.0% (	0.1% (%) 0.1% (%) 0.1% (%) 0.1% (%) 0.0% (%)
10 <sup>5</sup>	3.0.0 ° ° ° ° ° ° ° ° ° ° ° ° ° ° ° ° ° °	10.000	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
unknown Linestamp 2/25/16	<ul> <li>250 Block Header</li> <li>1250 Block Trailer</li> <li>1250 Block Trailer</li> <li>1250 Block Trailer</li> <li>1250 Furger Time</li> <li>1250 Pulse Raw Data</li> <li>1250 Pulse Integral</li> <li>1250 Pulse Pedestal</li> </ul>	1250 Event Trailer 1250 Data Not Valid 1255 Filler Word 1255 Block Header 1125 Block Trailer 1125 Event Header 1125 Event Header 1125 Vindow Raw Data 1125 Pulse Raw Data 1125 Pulse Raw Data 1125 Pulse Integral	F125 Fulse Time f125 Fulse Pedestal f125 Pulse Pedestal f125 Pulse Pedestal f125 Pulse Pedestal f125 Data Not Valid f125 Data Not Valid f125 Data Not Valid f125 Event Header F1v2 Block Header F1v2 Event Header F1v2 Chip Header F1v2 Chip Header F1v2 Chip Header F1v2 Chip Header F1v2 Ereat Word	F1v3 Block Header F1v3 Block Trailer F1v3 Event Header F1v3 Event Header F1v3 Trigger Time F1v3 Trigger Time F1v3 Braak Word F1v3 Break Word F1v3 Break Word CAEN1190 GLobal Header CAEN1190 GLobal Header CAEN1190 TDC Header CAEN1190 TDC Header CAEN1190 TDC Header CAEN1190 TDC Header CAEN1190 TDC Trailer CAEN1190 TDC Trailer CAEN1190 TDC Trailer CAEN1190 TDC Trailer CAEN1190 TDC Trailer	DAQ Config DAQ Config 1250 DAQ Config 125 DAQ Config 125 DAQ Config 125 DAQ Config 125 DAQ Config 125 DAQ Config 125 0xt000d00d Total words in all events Number of events

Adjusting profile of 2014 commissioning data based on recent or planned firmware upgrades is used to estimate event size for production data in the future.



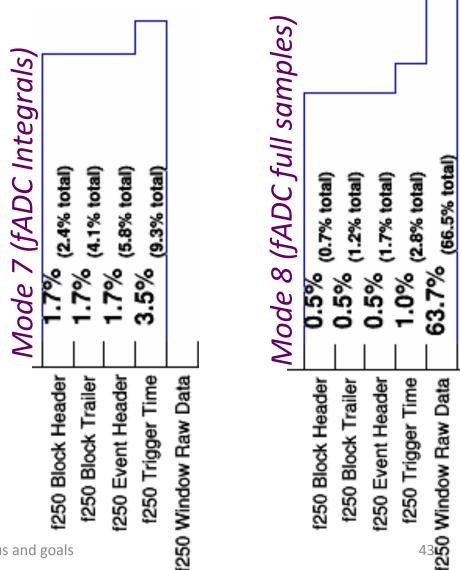
(Additional compression is expected when disentangled data is rebuilt after L3 into an as yet undetermined format.)

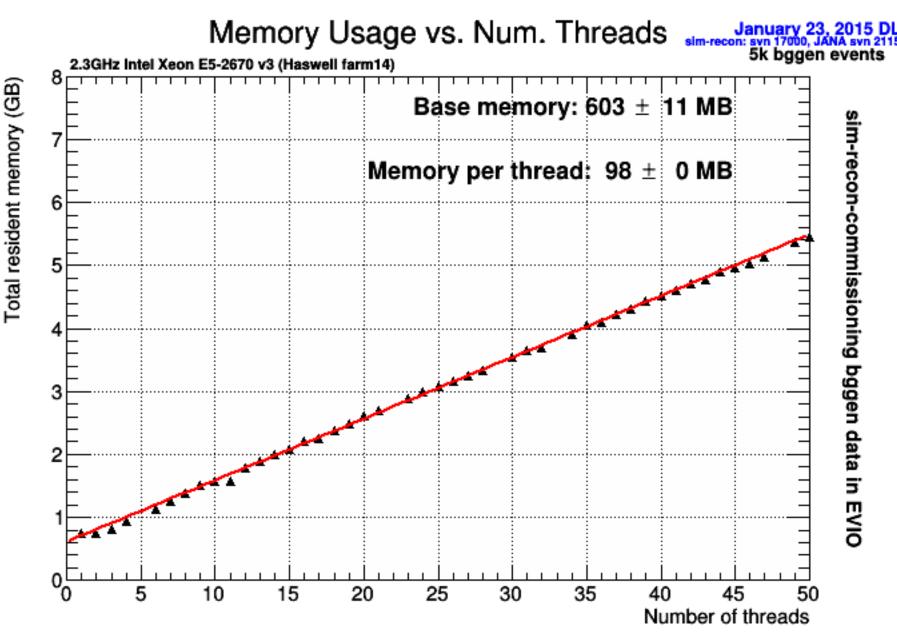
## Counting house computer systems

Computer(s)	processor	General Purpose Network	DAQ Network	I.B. Network	comments
gluonfs1	N/A	Х			~1.6TB with snapshot backup
gluonraid1-2	Intel E5-2630 v2 @2.6GHz	Х	Х	Х	RAID disk host ER process
gluon01-05	i5-3570 @3.4GHz	Х			Shift taker consoles
gluon20-23	AMD 2347	Х			Controls 8core
gluon24-30	E5-2420 @1.9GHz	Х			Controls (gluon24 is web/DB/cMsg server) 12core + 12ht
gluon40-43	AMD 6380	Х	Х	Х	16core + 16"ht"
gluon46-49	E5-2650 v2 @2.6GHz	Х	<b>X</b> (gluon47 &49)	Х	16core + 16ht
gluon100-111	E5-2650 v2 @2.6GHz	Х		Х	16core + 16ht
rocdev1	Pentium 4 @2.8GHz	Х			RHEL5 system for compiling ROLs for DAQ
hdguest0-3	i5-3470 @3.2GHz	X (outside network)	<del>L3 status an</del>	d-goals	Guest consoles in cubicles (outside network)

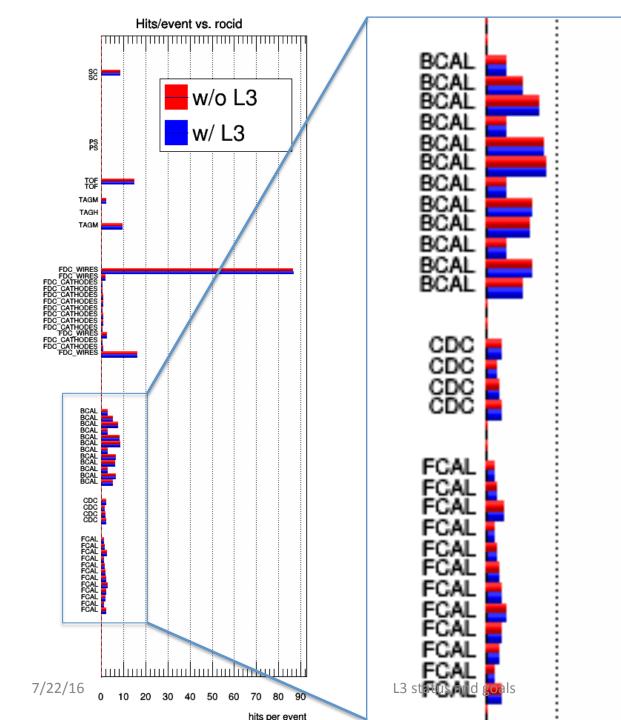
Example: some of the fADC250 *word types* 

- Each 32bit word in the EVIO file tallied to identify what file space is being used for
- Comparison between mode 7 and mode 8 data made





name	node	level 🗸	Nthr	Nevents	rate (Hz)	idle
gluon101.jlab.org_11057	gluon101.jlab.org		32	212874	751.1	75.2%
gluon49.jlab.org_12748	gluon49.jlab.org		32	206140	646.7	22.8%
gluon111.jlab.org_5726	gluon111.jlab.org		32	206700	643.1	58.1%
gluon110.jlab.org_30100	gluon110.jlab.org		32	208726	700.7	4.1%
gluon109.jlab.org_4409	gluon109.jlab.org		32	213494	369.9	75.2%
gluon108.jlab.org_10935	gluon108.jlab.org		32	247875	390.6	77.0%
gluon107.jlab.org_23963	gluon107.jlab.org		32	211054	406.0	72.1%
gluon106.jlab.org_20172	gluon106.jlab.org		32	219621	895.5	49.8%
gluon105.jlab.org_10192	gluon105.jlab.org		32	212474	507.2	71.1%
gluon104.jlab.org_23134	gluon104.jlab.org		32	205814	549.7	66.7%
gluon102.jlab.org_22561	gluon102.jlab.org		32	206803	451.5	71.7%
gluon48.jlab.org_18860	gluon48.jlab.org		32	214251	519.2	49.7%



Comparing number of hits before and after rewriting with L3