

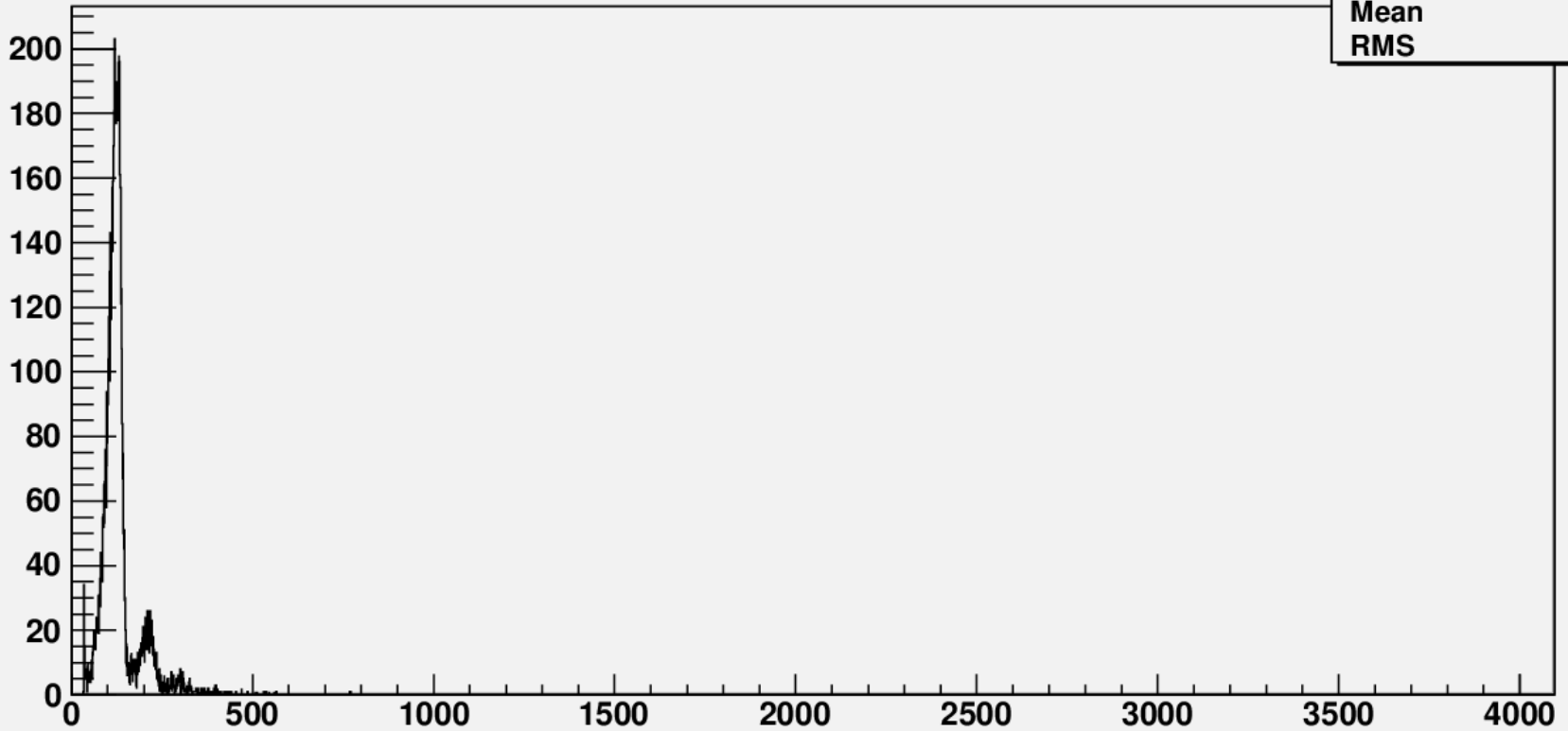
# Pedestal measurements at different gate width

USM update  
MPPC Test Setup  
Stage 3

13 – 03 – 2012

# Gate 120ns

hist\_mppc2\_cell5



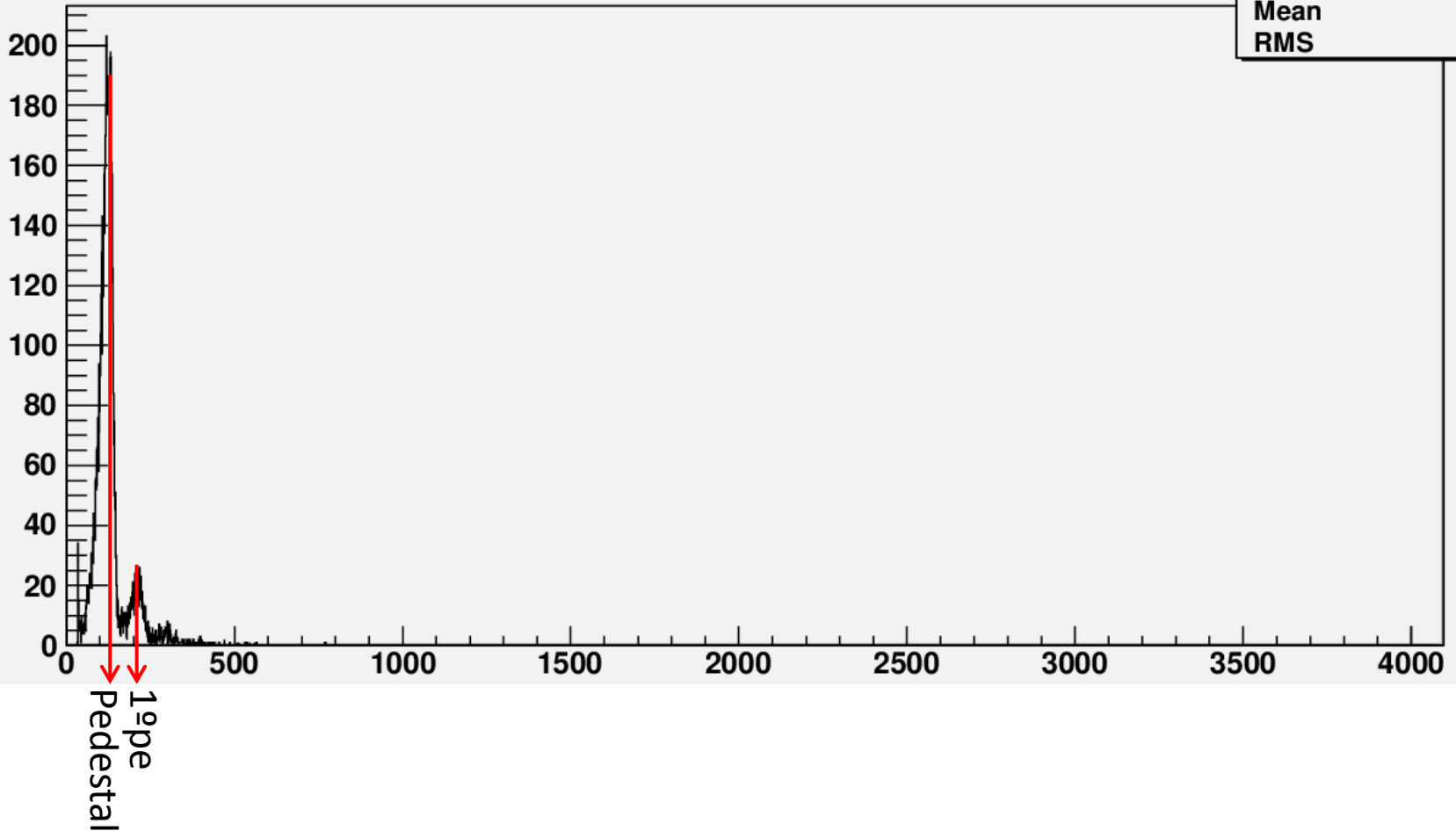
hist\_mppc2\_cell5

Entries	10000
Mean	129.5
RMS	50.82

MPPC id: 01911

# Gate 120ns

hist\_mppc2\_cell5

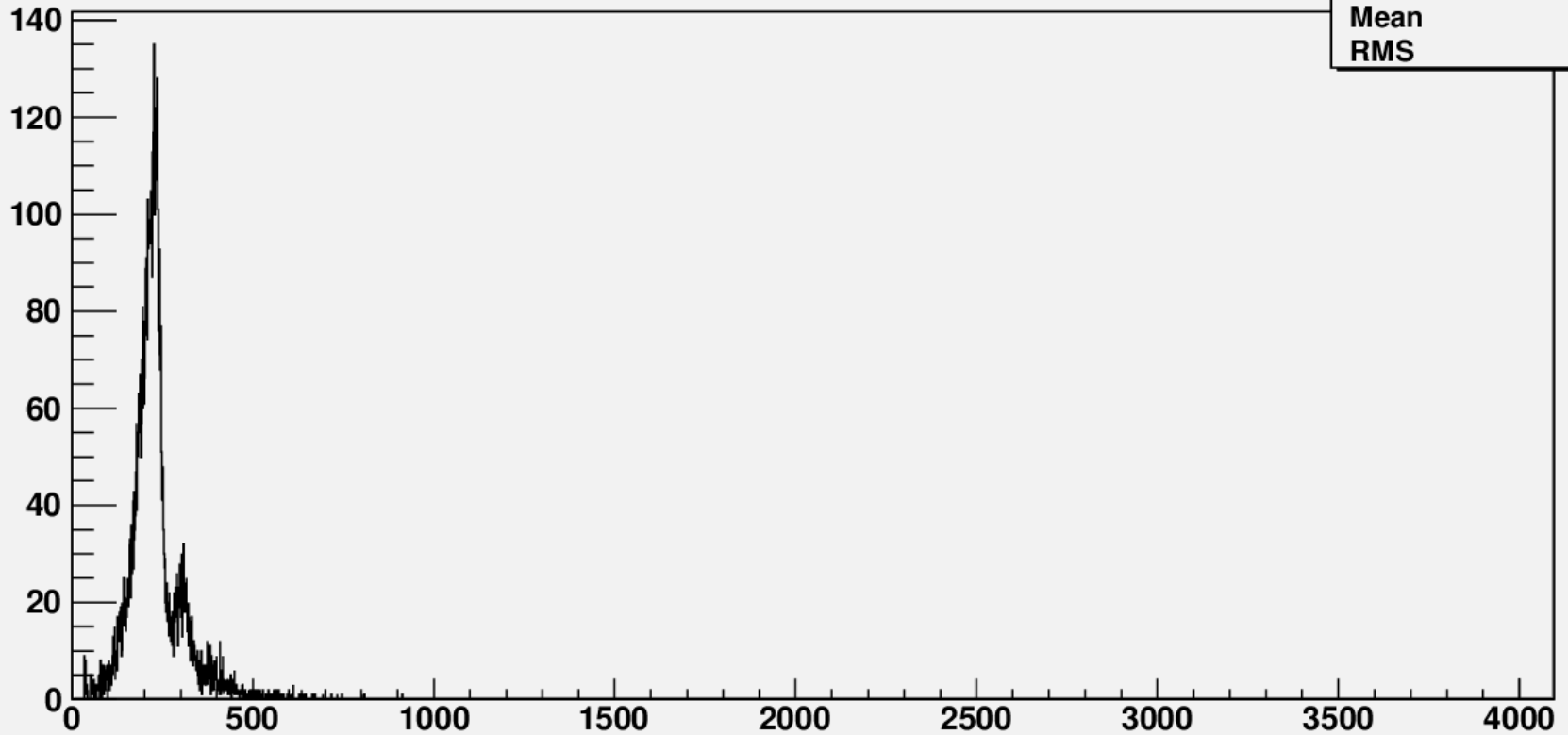


hist\_mppc2\_cell5

Entries	10000
Mean	129.5
RMS	50.82

# Gate 240ns

hist\_mppc2\_cell5

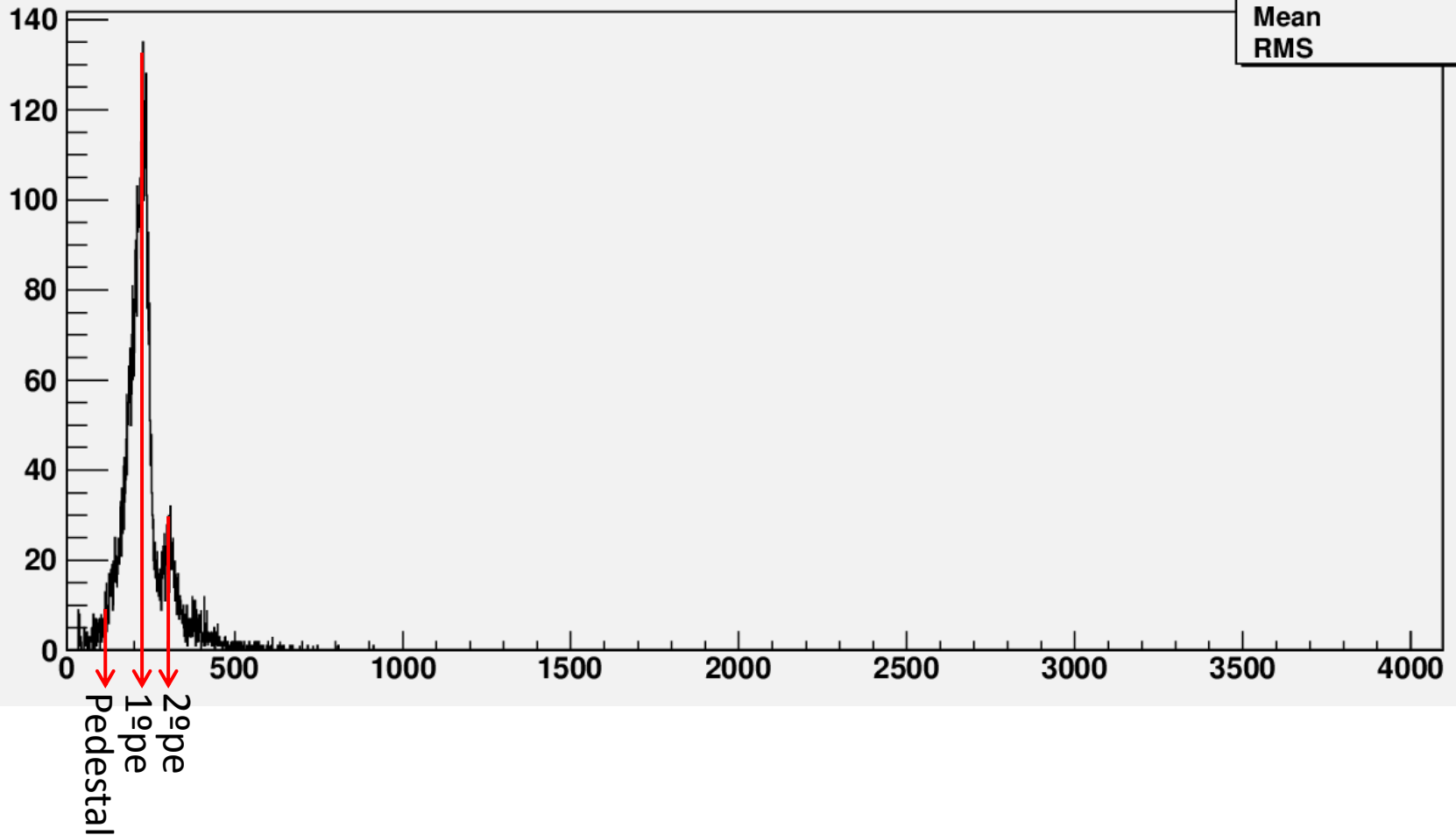


hist\_mppc2\_cell5

Entries	10000
Mean	229.5
RMS	71.97

# Gate 240ns

hist\_mppc2\_cell5

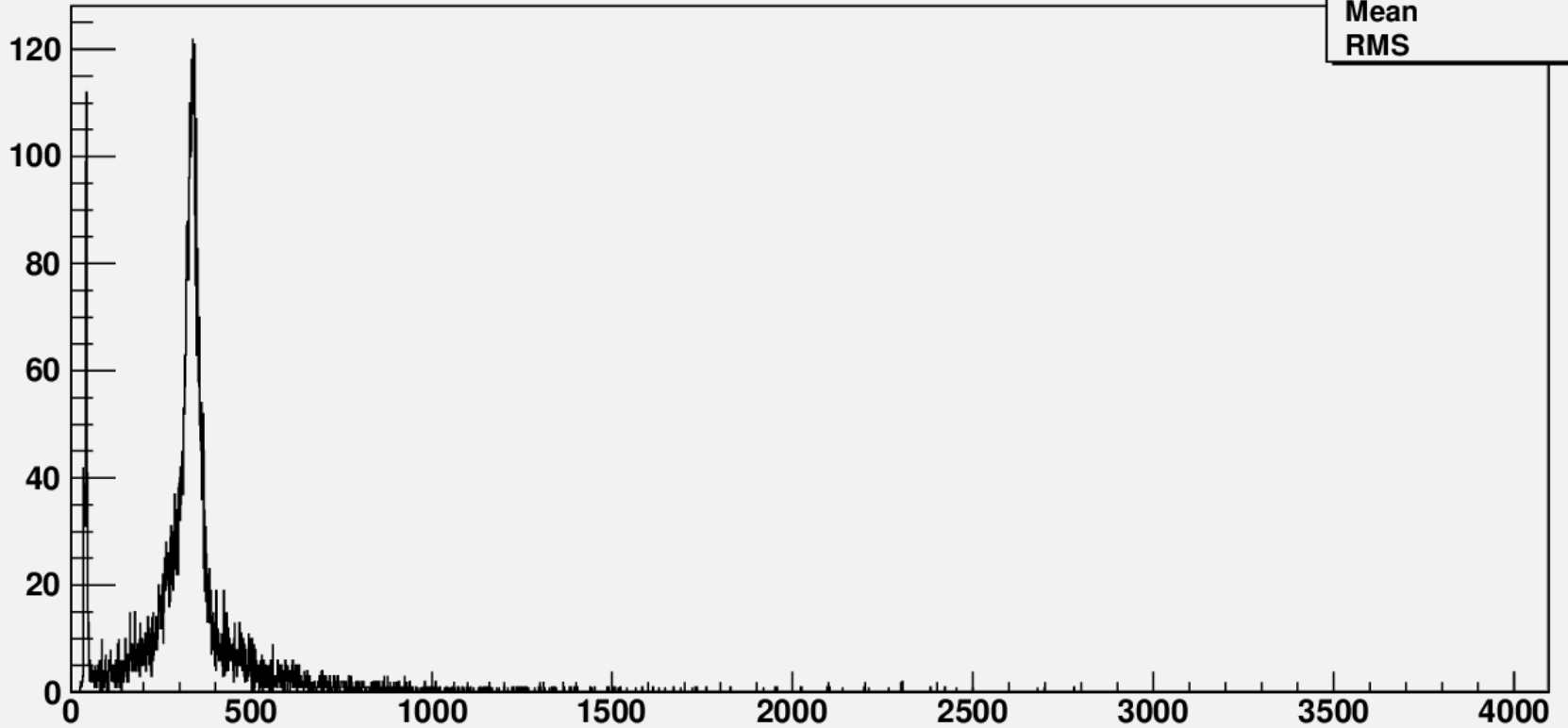


hist\_mppc2\_cell5

Entries	10000
Mean	229.5
RMS	71.97

# Gate 360ns

hist\_mppc2\_cell5

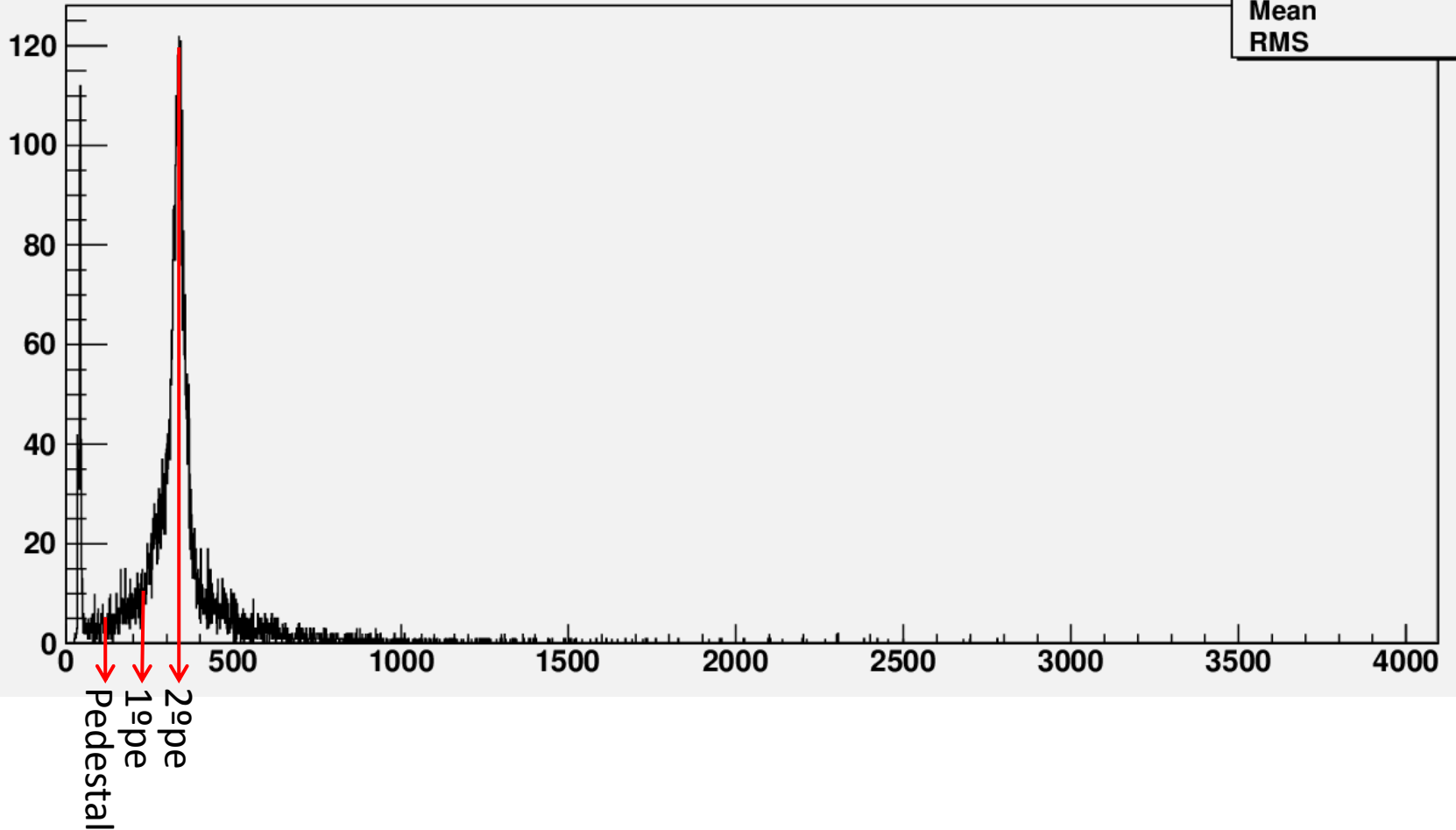


hist\_mppc2\_cell5

Entries	10000
Mean	336.7
RMS	193.5

# Gate 360ns

hist\_mppc2\_cell5



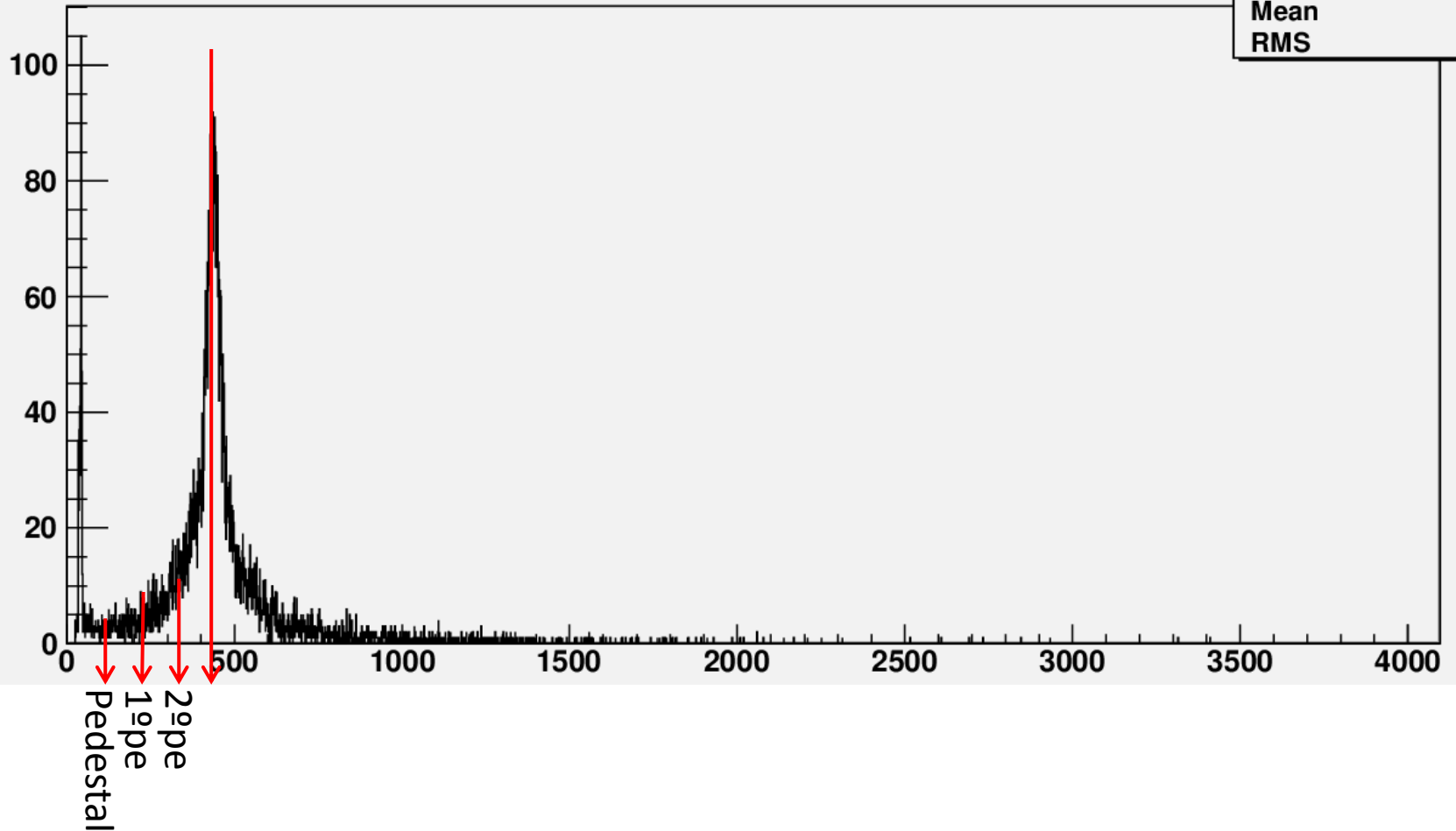
hist\_mppc2\_cell5

Entries	10000
Mean	336.7
RMS	193.5

# Gate 480ns

hist\_mppc2\_cell5

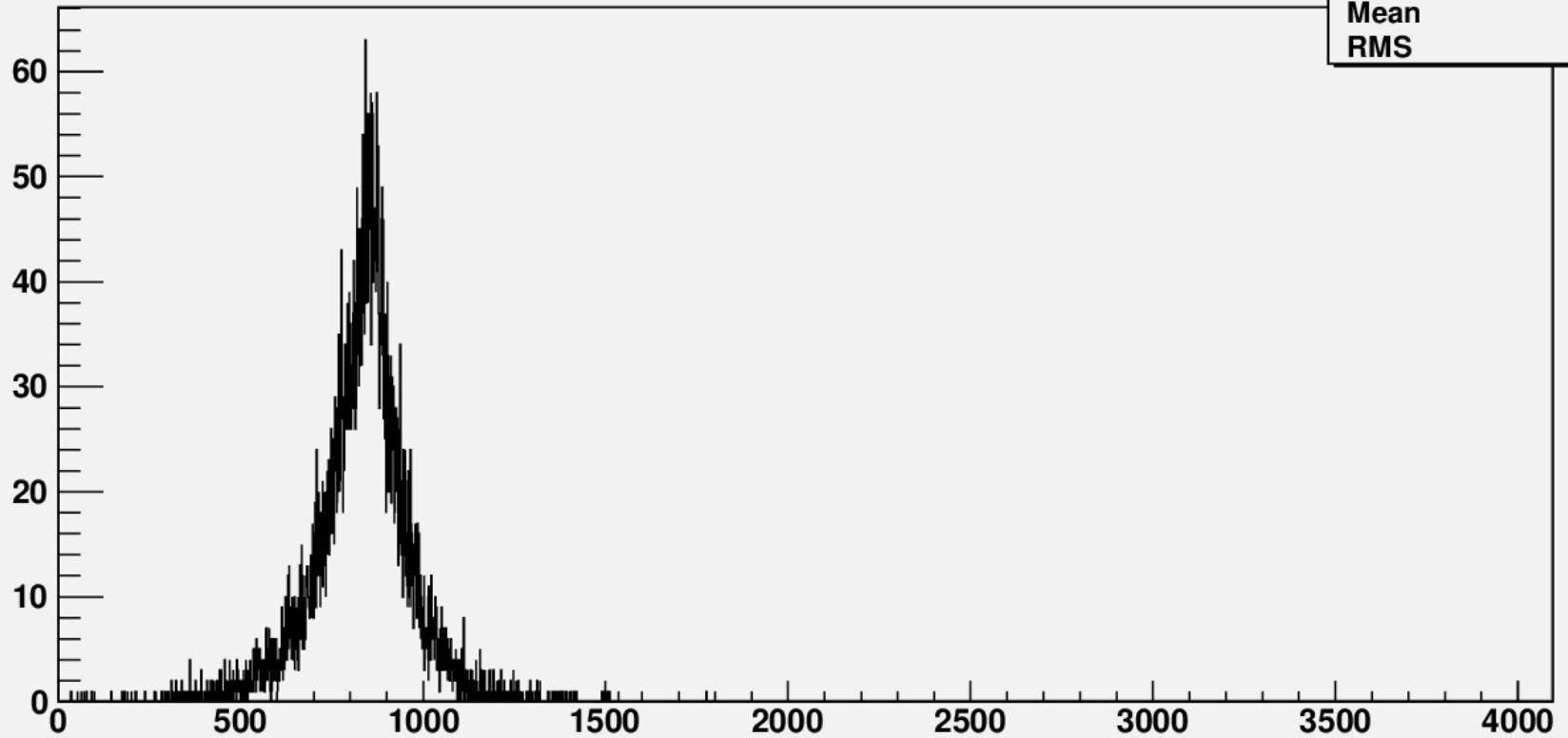
hist_mppc2_cell5	
Entries	10000
Mean	437.5
RMS	233.5





# Gate 1us

hist\_mppc2\_cell5



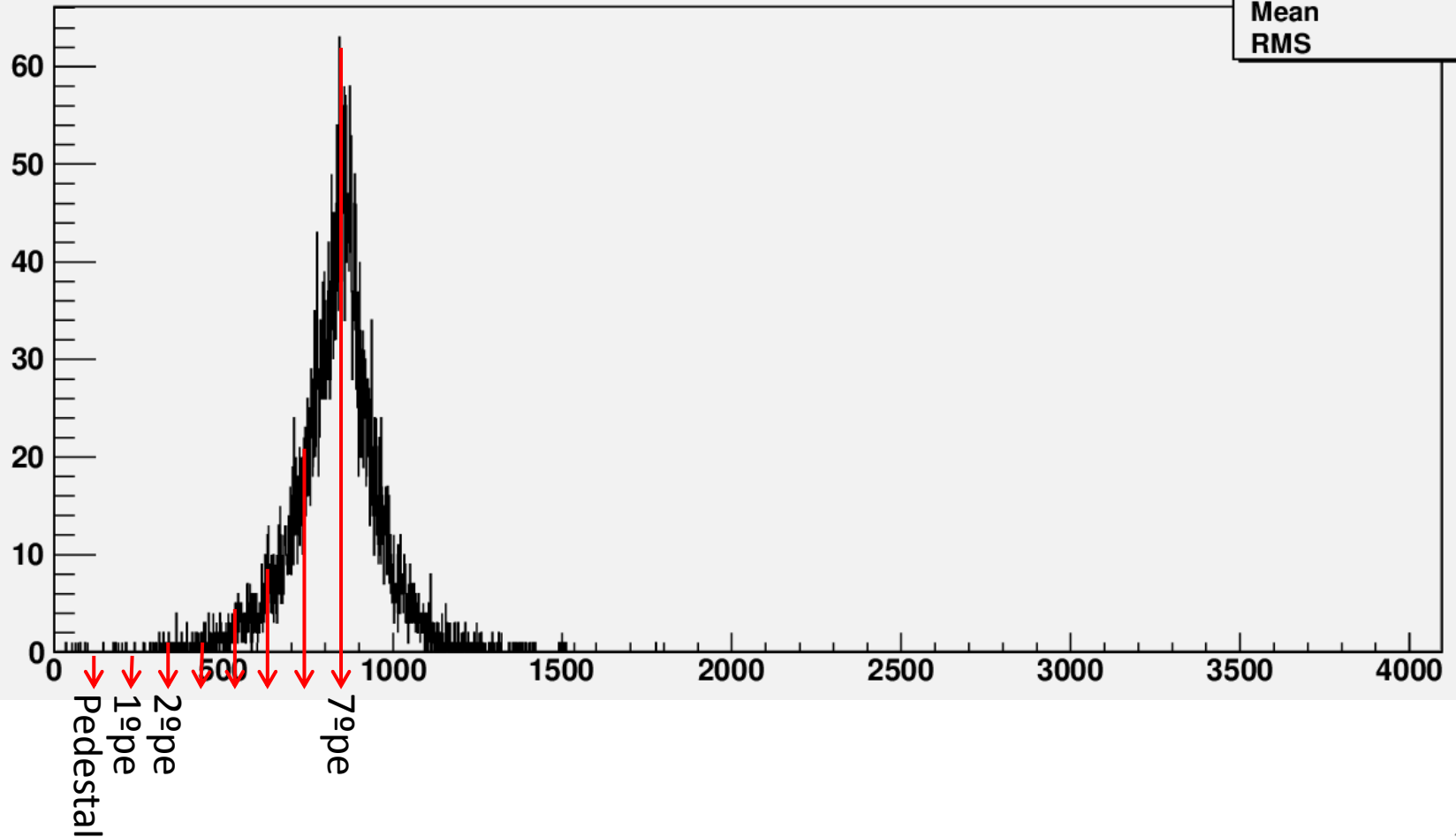
hist\_mppc2\_cell5

Entries	10000
Mean	831.8
RMS	131.6

# Gate 1us

hist\_mppc2\_cell5

hist_mppc2_cell5	
Entries	10000
Mean	831.8
RMS	131.6



**END**