

	Estimated Quantity	Est. Unit cost (\$)	Est. Total Cost (K\$)	Developer/ Vendor	Comment
<b>Readout System</b>					
MaPMT	220	1910	420.2	Hamamatsu	Cost depends on JPY/USD exchange rate
MAROC ASIC chip	220	120	26.4	Omega group	1K chips recently produced, 400 will be used by CLAS12
Adapter+ASIC boards	80	240	19.2	INFN/EES	Expect to start CLAS12 production in October 2015
FPGA board	80	700	56	JLab Fast Elec.	Expect to start CLAS12 production in October 2015
<b>DAQ</b>					
SSP	3	8500	25.5		
VXS Crate	1	15000	15		
Fiber	19	500	9.5		
<b>HV, LV, cables</b>					
HV module	10	7000	70		
HV chasis	1	8500	8.5		
HV coax cables	220	75	16.5		
LV module	2	3500	7		
LV cables	4	250	1		
Ground cable	1	1000	1		
<b>Mechanical Structure</b>					
<b>Shipment to JLab</b>					
<b>Optical System</b>					
Mirrors					
Focusing box					
<b>Total</b>			675.8		