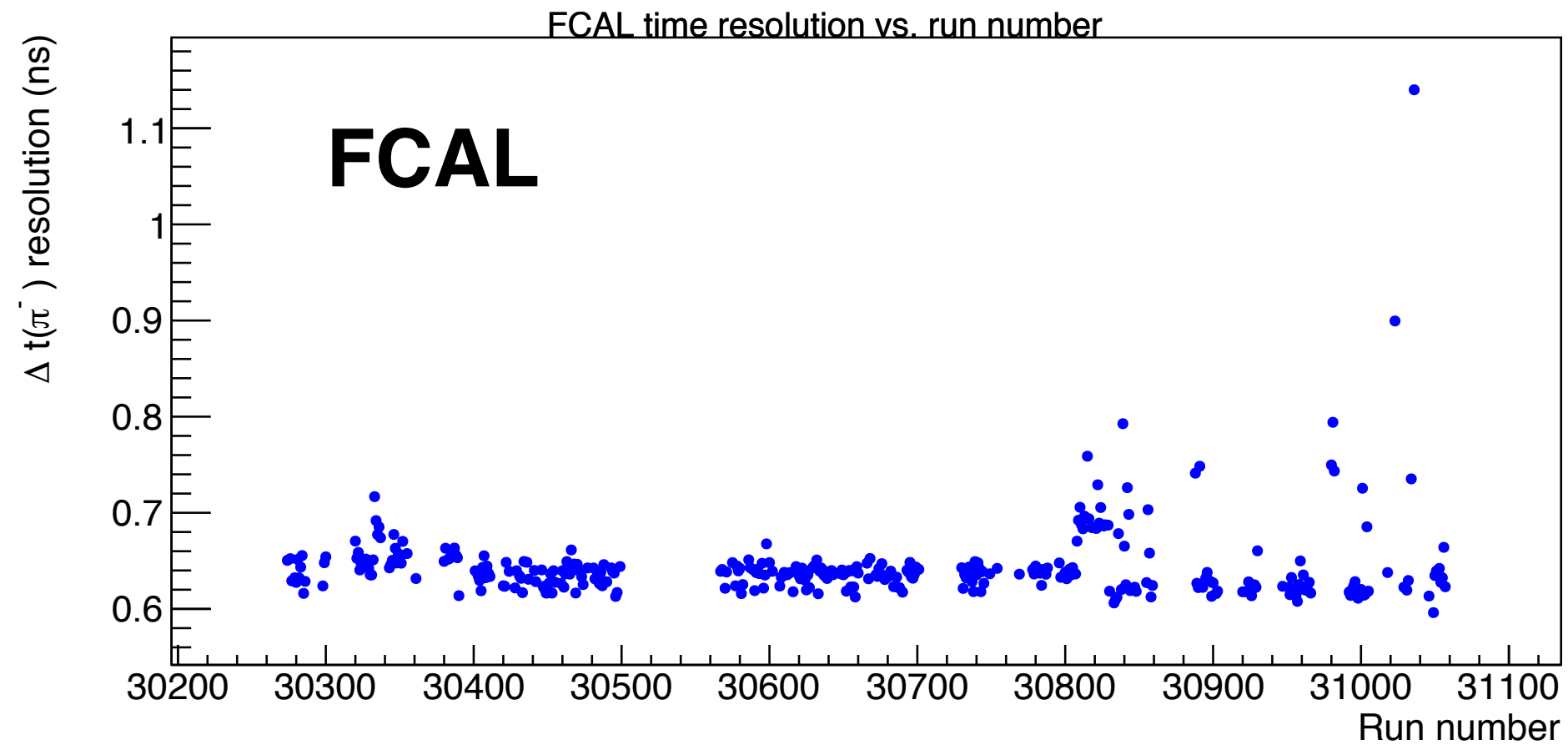
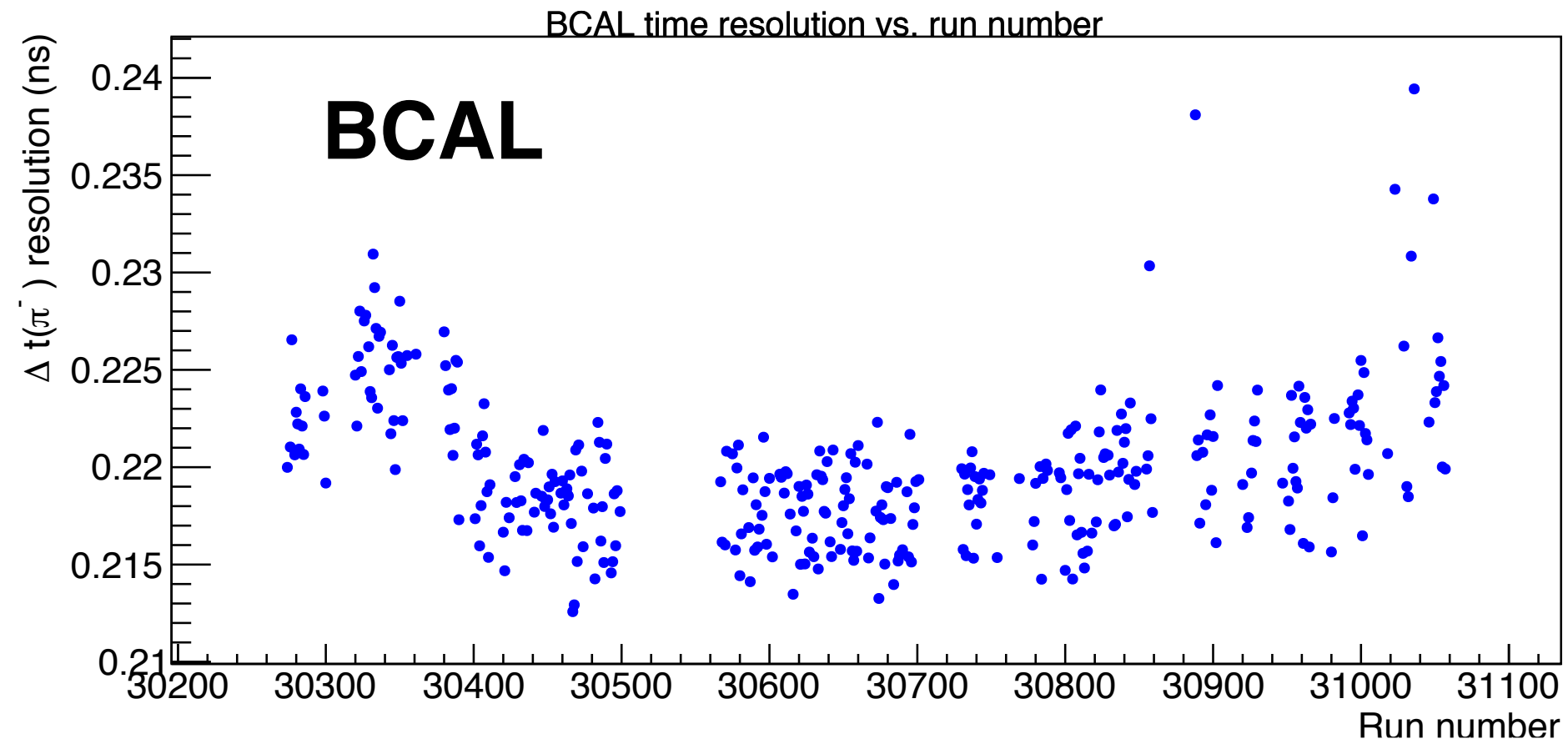


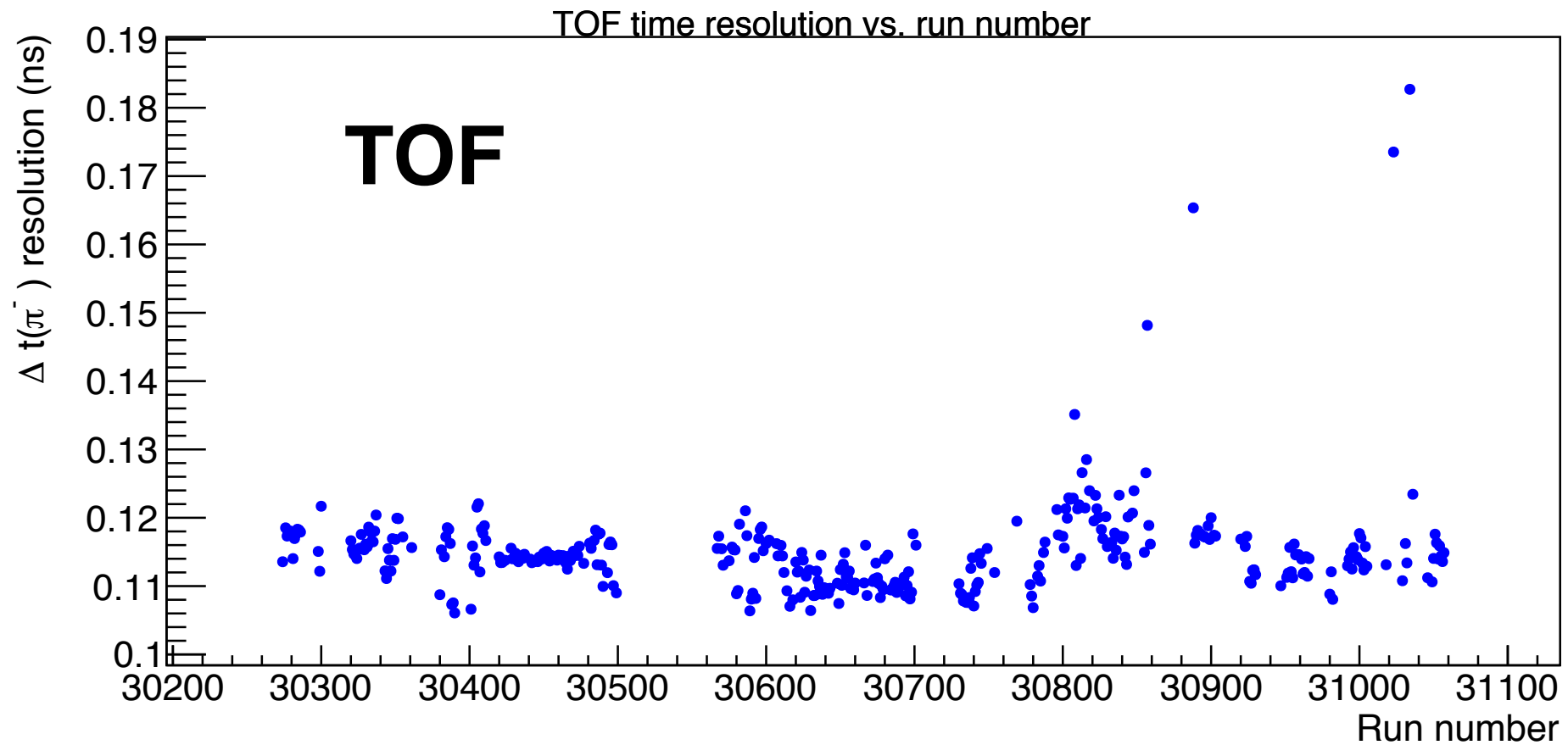
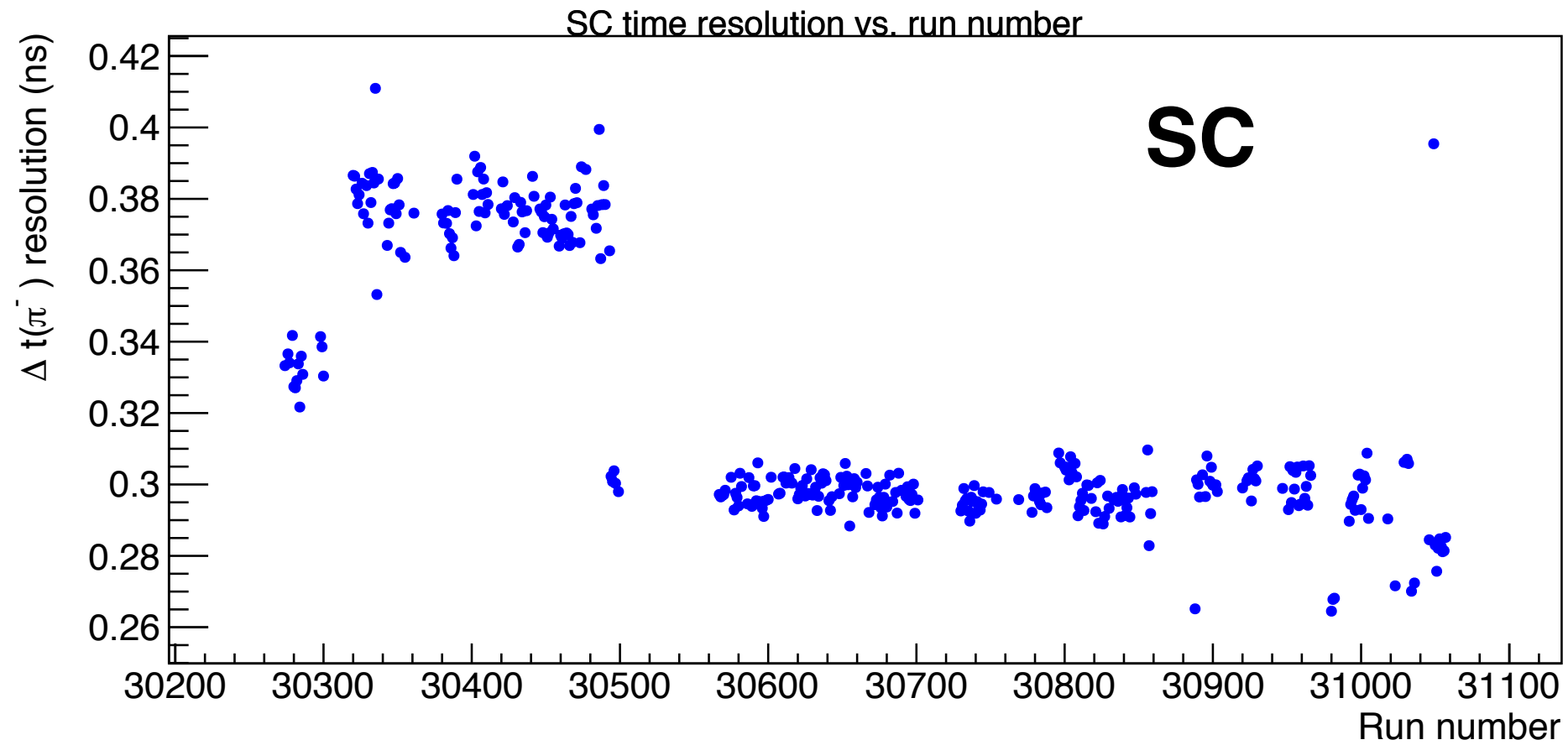
Calibration Status

- Timing calibrations still being finalized
 - Note that 4 ns shifts are due to ADCs not TDCs
 - Calorimeters: checking using TDCs and LED data
 - Nathan doing tagger/PS fine tuning
- Gain calibrations
 - FCAL: Done
 - BCAL: Finalizing
 - CDC: Checking a few last runs
- DC Alignment?
- Schedule status?
- F1TDC unlocking: only affects FDC, correlated with noise

BCAL/FCAL time resolution



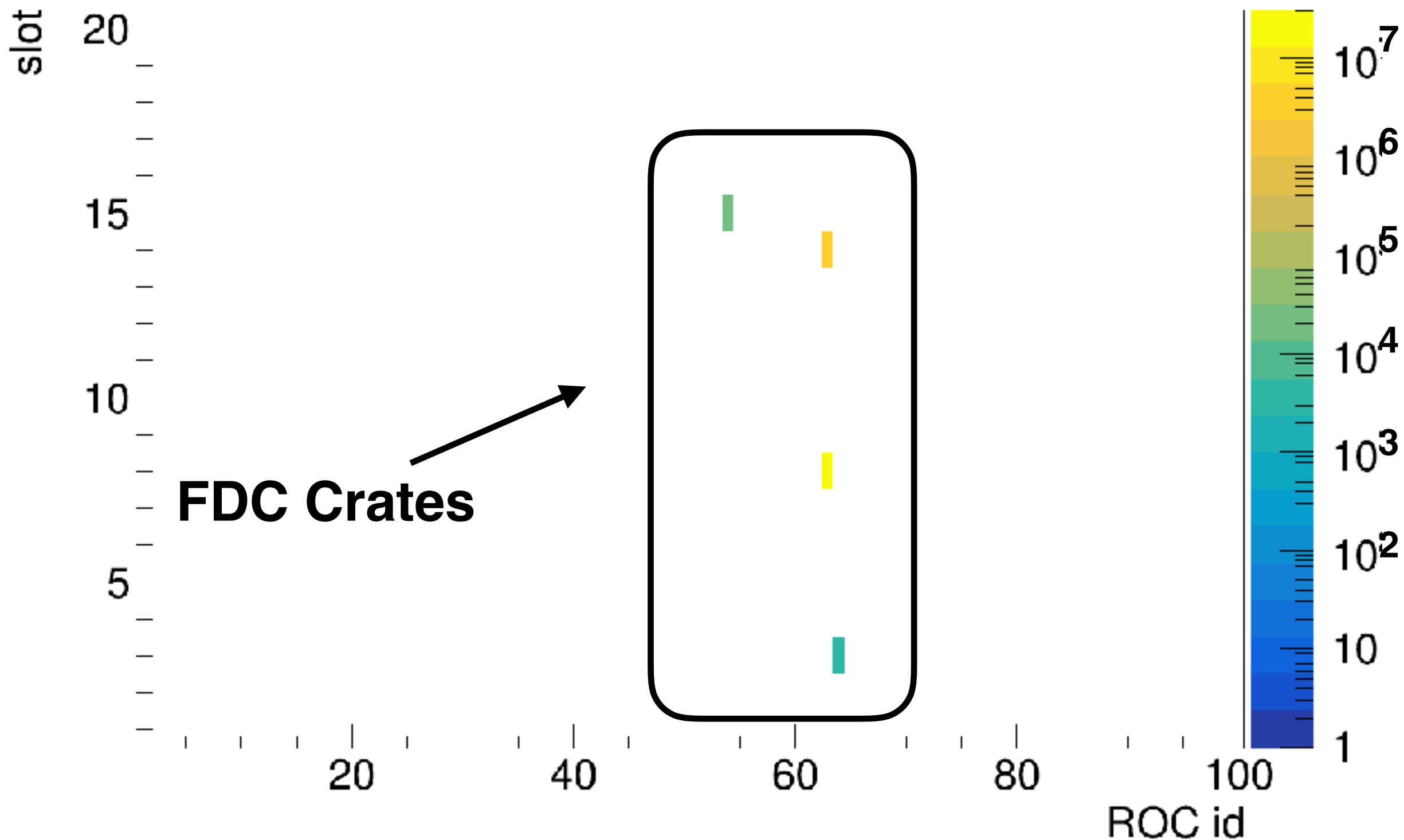
SC/TOF time resolution



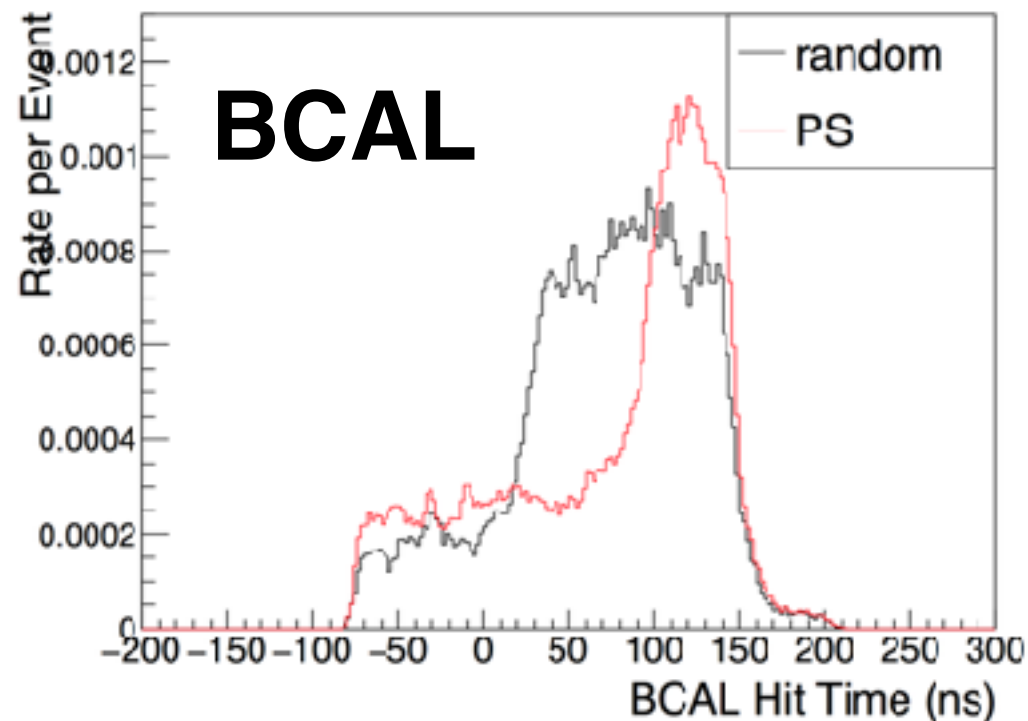
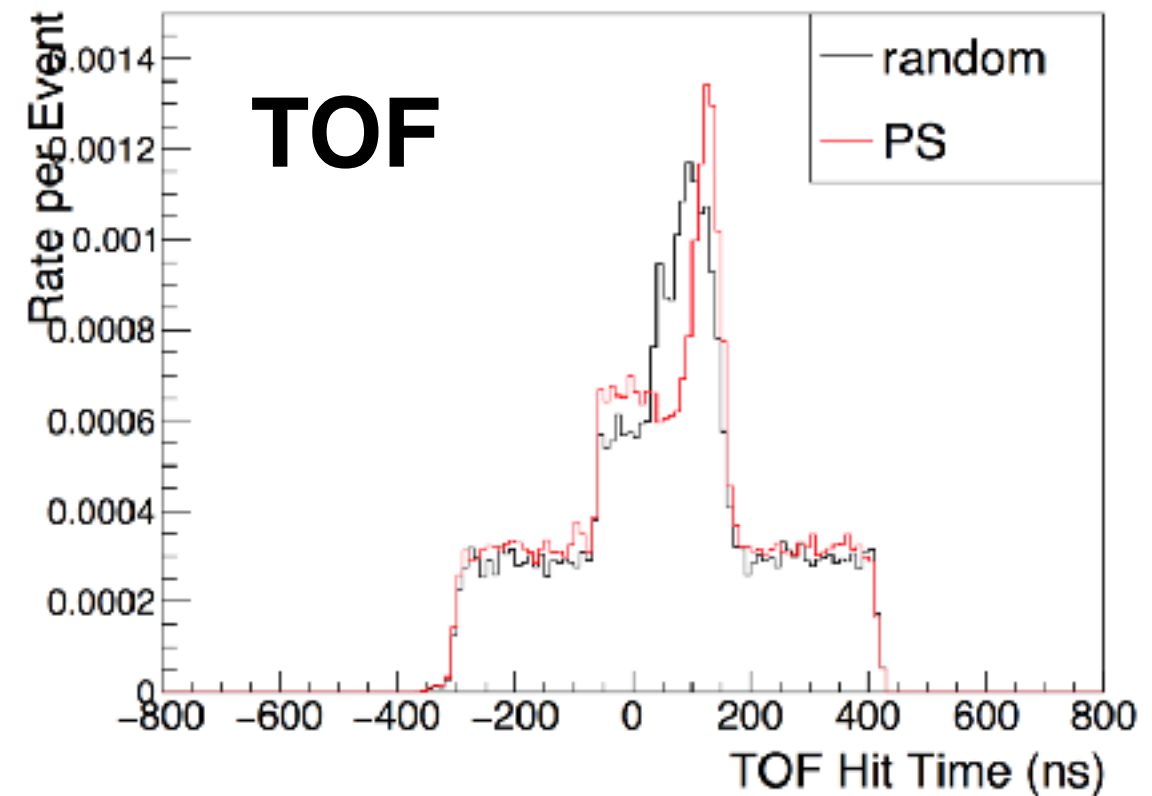
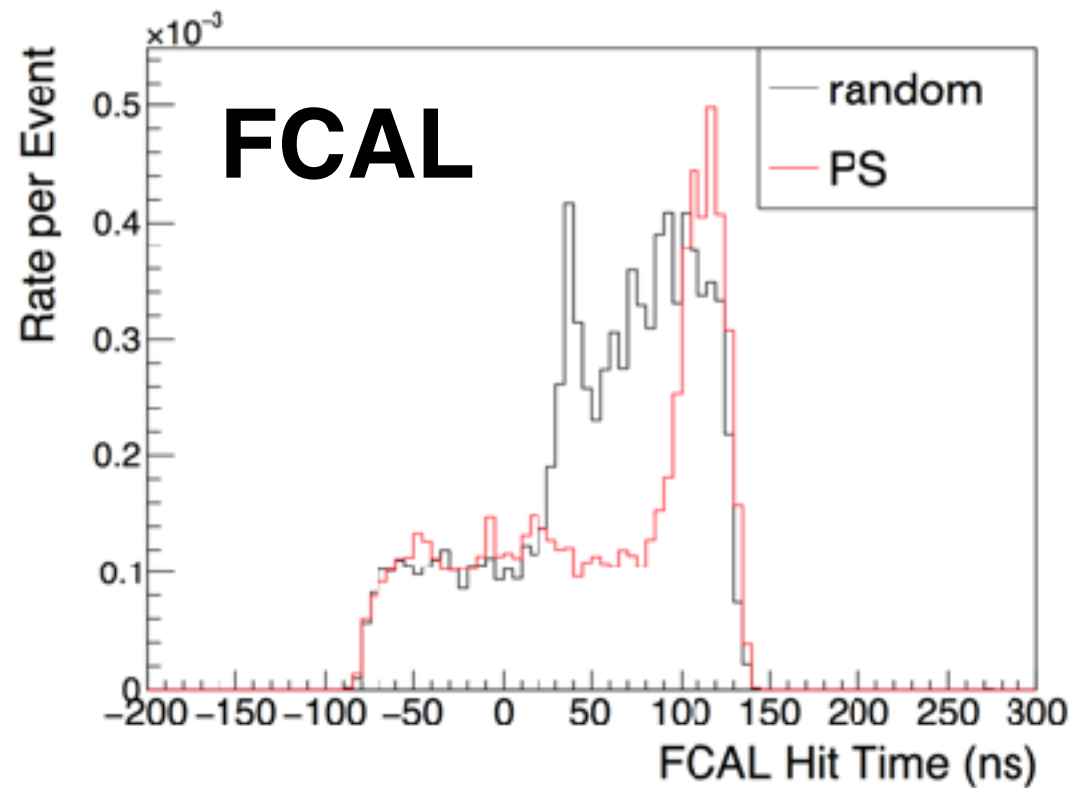
F1TDC Unlocking

Run 31005

F1TDC hits unlocked



Random vs. PS triggers



Rates are generally the same, time structure different
See: <https://logbooks.jlab.org/entry/3469212>